

## Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

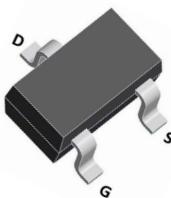
## Product Summary



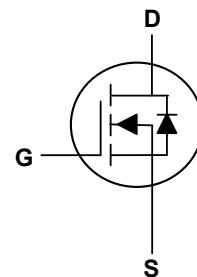
$V_{DS}$	30	V
$I_D$	1.7	A
$R_{DS(ON)}$ (at $V_{GS}=10V$ )	55	mΩ
$R_{DS(ON)}$ (at $V_{GS}=4.5V$ )	65	mΩ

## Applications

- High Frequency Point-of-Load Synchronous Buck Converter
- Networking DC-DC Power System
- Power Tool Application



SOT-323 Top View



## Absolute Maximum Ratings( $T_A=25^\circ C$ , unless otherwise noted)

Parameter	Symbol	Rating	Units
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current <sup>1</sup>	$I_D @ T_C = 25^\circ C$	1.7	A
Continuous Drain Current <sup>1</sup>	$I_D @ T_C = 70^\circ C$	1.3	A
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	15	A
Total Power Dissipation <sup>4</sup>	$P_D$	0.35	W
Storage Temperature Range	$T_{STG}$	-55 to 150	°C
Operating Junction Temperature Range	$T_J$	-55 to 150	°C

## Thermal Characteristics

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance Junction-Ambient <sup>1</sup>	$R_{\theta JA}$	---	425	°C/W

**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$	30	---	---	V
Static Drain-Source On-Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}$ , $I_D=1.7\text{A}$	---	45	55	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$ , $I_D=1.5\text{A}$	---	50	65	$\text{m}\Omega$
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D=250\mu\text{A}$	0.5	---	1.5	V
Drain-Source Leakage Current	$I_{\text{DSS}}$	$V_{\text{DS}}=30\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	1	$\mu\text{A}$
Gate-Source Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm 12\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA
Forward Transconductance	$R_{\text{fs}}$	$V_{\text{DS}}=5\text{V}$ , $I_D=1.7\text{A}$	---	14	---	S
Total Gate Charge	$Q_g$	$V_{\text{DS}}=15\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $I_D=1.7\text{A}$	---	12	---	nC
Gate-Source Charge	$Q_{\text{gs}}$		---	0.8	---	
Gate-Drain Charge	$Q_{\text{gd}}$		---	1.4	---	
Turn-On Delay Time	$T_{\text{d}(\text{on})}$	$V_{\text{DS}}=15\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $R_G=3\Omega$	---	4	---	ns
Rise Time	$T_r$		---	2	---	
Turn-Off Delay Time	$T_{\text{d}(\text{off})}$		---	18	---	
Fall Time	$T_f$		---	3	---	
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=15\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	244	---	pF
Output Capacitance	$C_{\text{oss}}$		---	35	---	
Reverse Transfer Capacitance	$C_{\text{rss}}$		---	16	---	

**Drain-Source Diode Characteristics**

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Diode Forward Voltage <sup>2</sup>	$V_{\text{SD}}$	$V_{\text{GS}}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	1.1	V

**Note:**

- 1.Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 3.The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$

## Typical Characteristics

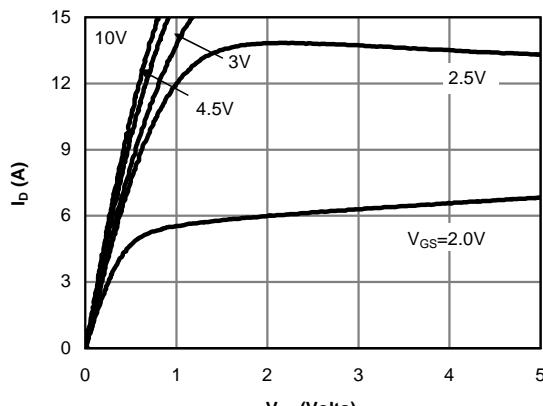


Fig 1: On-Region Characteristics (Note E)

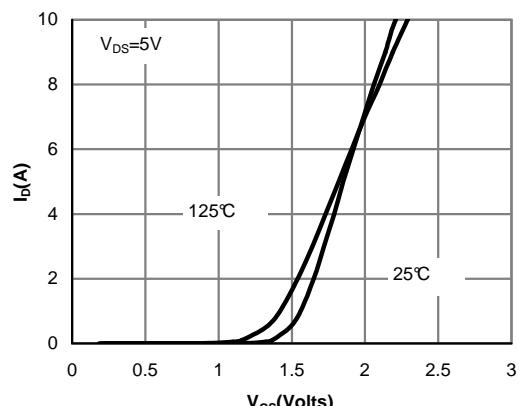


Figure 2: Transfer Characteristics (Note E)

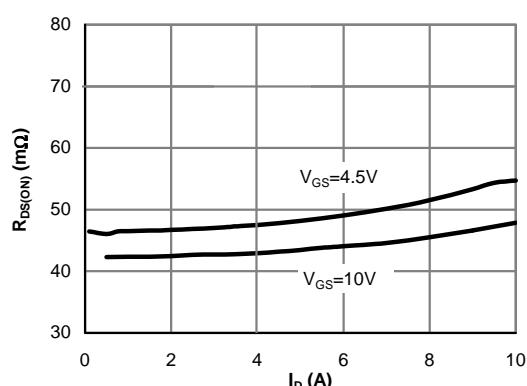


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

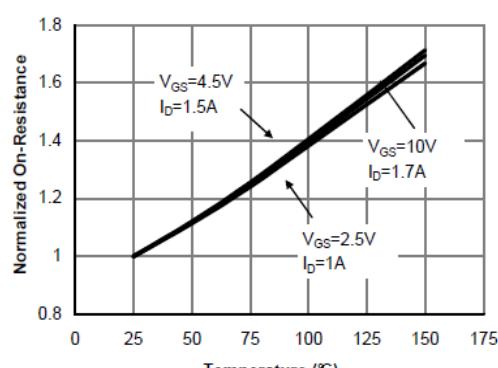


Figure 4: On-Resistance vs. Junction Temperature (Note E)

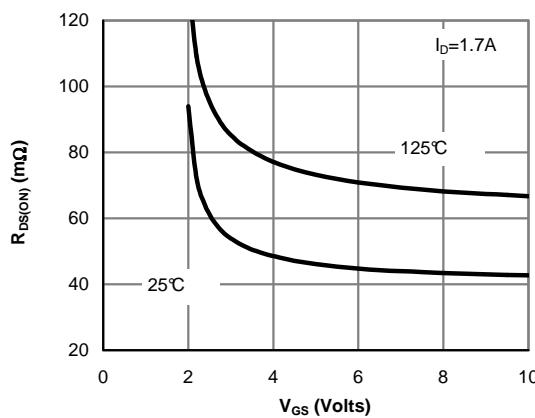


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

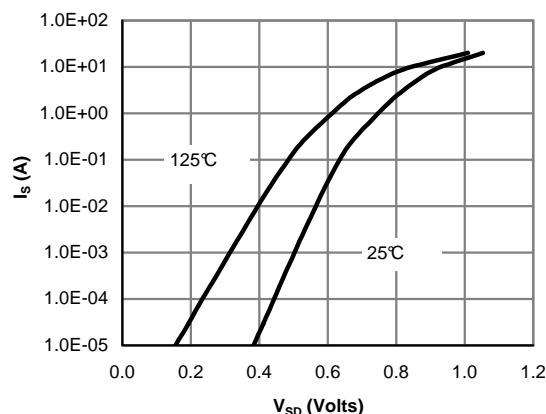
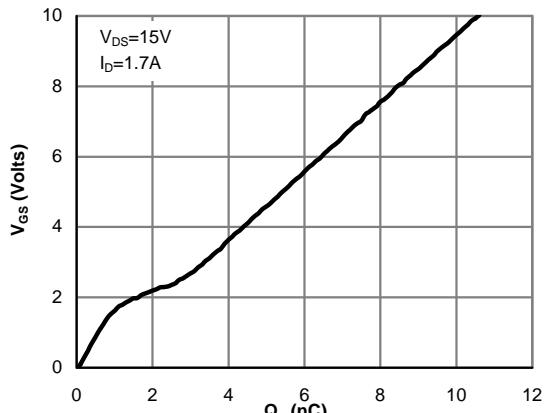
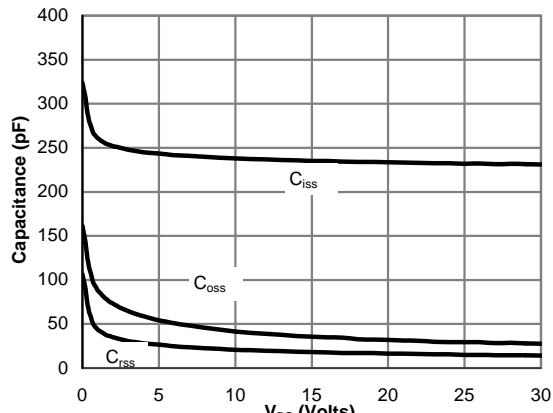


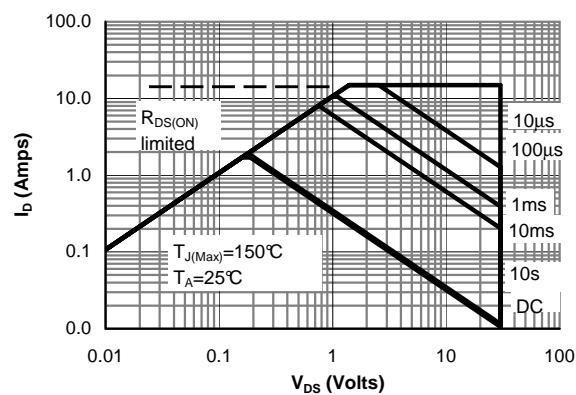
Figure 6: Body-Diode Characteristics (Note E)



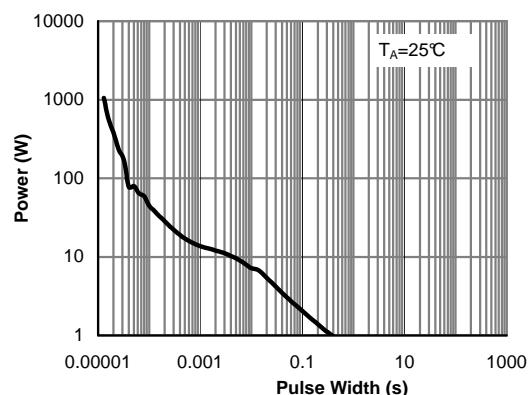
**Figure 7: Gate-Charge Characteristics**



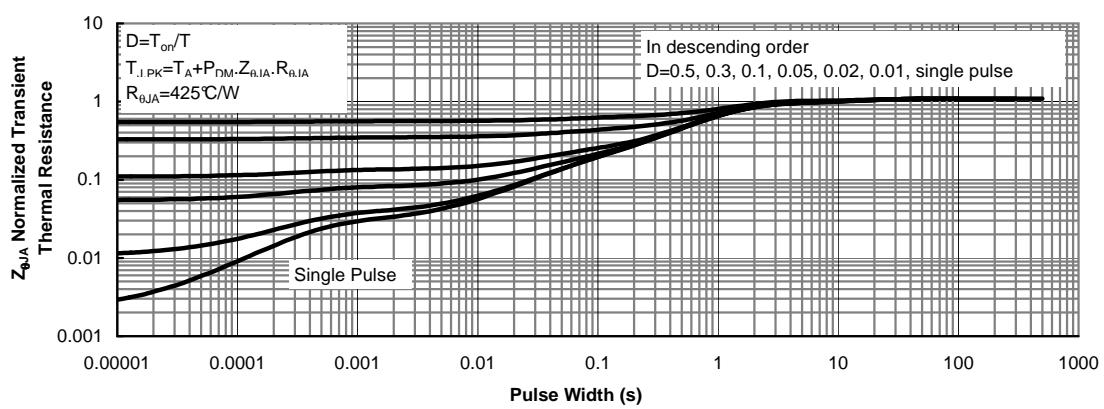
**Figure 8: Capacitance Characteristics**



**Figure 9: Maximum Forward Biased Safe Operating Area (Note F)**

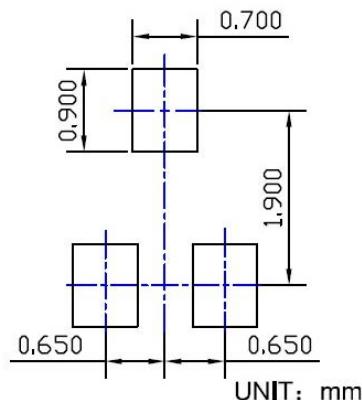
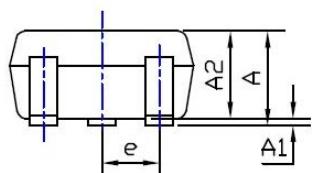
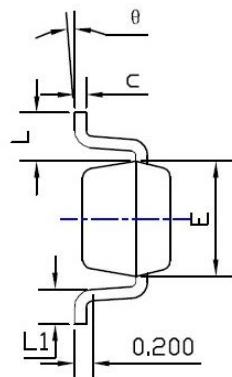
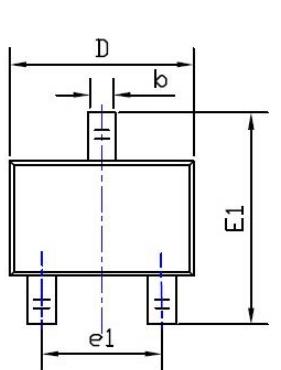


**Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note F)**



**Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)**

### SOT-323 Package Outline Dimensions



<b>Symbol</b>	<b>Dimensions (unit:mm)</b>			<b>Symbol</b>	<b>Dimensions (unit:mm)</b>		
	<b>Min</b>	<b>Typ</b>	<b>Max</b>		<b>Min</b>	<b>Typ</b>	<b>Max</b>
<b>A</b>	0.90	1.00	1.10	<b>E<sub>1</sub></b>	2.15	2.30	2.45
<b>A<sub>1</sub></b>	--	--	0.10	<b>e</b>	--	0.65	--
<b>A<sub>2</sub></b>	0.90	0.95	1.00	<b>e<sub>1</sub></b>	1.20	1.30	1.40
<b>b</b>	0.15	0.30	0.40	<b>L</b>	--	0.525	--
<b>c</b>	0.10	0.17	0.25	<b>L<sub>1</sub></b>	0.26	0.36	0.46
<b>D</b>	1.80	2.00	2.20	<b>θ</b>	0°		8°
<b>E</b>	1.15	1.25	1.35				