

## Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

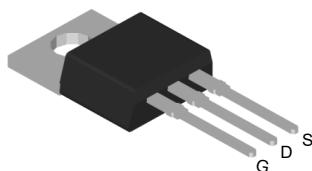
## Product Summary



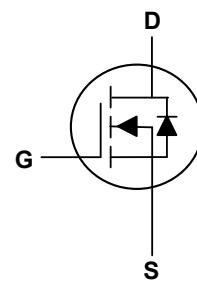
$V_{DS}$	150	V
$I_D$	200	A
$R_{DS(ON)}$ (at $V_{GS}=10V$ )	4.2	mΩ

## Applications

- High Frequency Point-of-Load Synchronous Buck Converter
- Networking DC-DC Power System
- Power Tool Application



TO-220 Top View



## Absolute Maximum Ratings( $T_c=25^\circ\text{C}$ , unless otherwise noted)

Parameter	Symbol	Rating	Units
Drain-Source Voltage	$V_{DS}$	150	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current <sup>1</sup>	$I_D @ T_c = 25^\circ\text{C}$	200	A
Continuous Drain Current <sup>1</sup>	$I_D @ T_c = 100^\circ\text{C}$	137	A
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	750	A
Single Pulse Avalanche Energy <sup>3</sup>	$E_{AS}$	1760	mJ
Total Power Dissipation	$P_D$	430	W
Storage Temperature Range	$T_{STG}$	-55 to 175	°C
Operating Junction Temperature Range	$T_J$	-55 to 175	°C

## Thermal Characteristics

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance Junction-Ambient <sup>1</sup>	$R_{\theta JA}$	---	60	°C/W
Thermal Resistance Junction-Case <sup>1</sup>	$R_{\theta JC}$	---	0.35	°C/W

**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$	150	---	---	V
Static Drain-Source On-Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}$ , $I_D=20\text{A}$	---	3.8	4.2	$\text{m}\Omega$
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D = 250\mu\text{A}$	2.0	---	4.0	V
Drain-Source Leakage Current	$I_{\text{DSS}}$	$V_{\text{DS}}=150\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	1	$\mu\text{A}$
Gate-Source Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	$\text{nA}$
Gate Resistance	$R_g$	$V_{\text{DS}}=0\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	2.7	---	$\Omega$
Total Gate Charge	$Q_g$	$V_{\text{DS}}=75\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $I_D=20\text{A}$	---	135	---	nC
Gate-Source Charge	$Q_{\text{gs}}$		---	44	---	
Gate-Drain Charge	$Q_{\text{gd}}$		---	18	---	
Turn-On Delay Time	$T_{\text{d}(\text{on})}$	$V_{\text{DS}}=75\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $R_g=3.3\Omega$ , $I_D=20\text{A}$	---	25	---	ns
Rise Time	$T_r$		---	12	---	
Turn-Off Delay Time	$T_{\text{d}(\text{off})}$		---	120	---	
Fall Time	$T_f$		---	40	---	
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=75\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=250\text{KHz}$	---	9980	---	pF
Output Capacitance	$C_{\text{oss}}$		---	780	---	
Reverse Transfer Capacitance	$C_{\text{rss}}$		---	6	---	

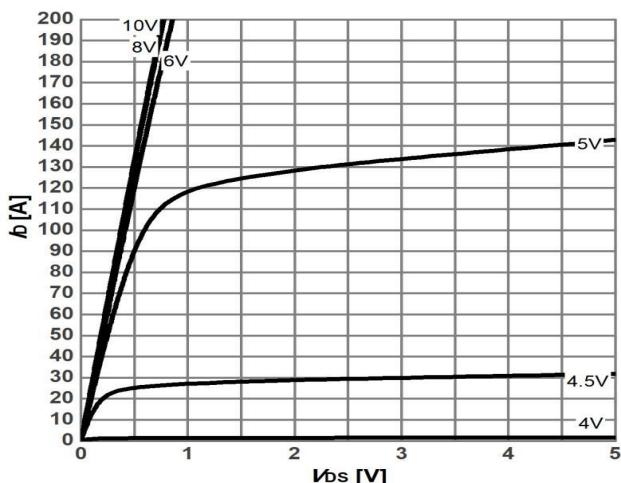
**Drain-Source Diode Characteristics**

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Diode Forward Voltage <sup>2</sup>	$V_{\text{SD}}$	$V_{\text{GS}}=0\text{V}$ , $I_s=10\text{A}$ , $T_J=25^\circ\text{C}$	---	---	1.2	V
Reverse recovery time	$t_{\text{rr}}$	$I_F=100\text{A}$ , $dI/dt=125\text{A}/\mu\text{s}$	---	90	---	ns
Reverse recovery charge	$Q_{\text{rr}}$		---	450	---	nC
Peak reverse recovery current	$I_{\text{rrm}}$		---	8	---	A

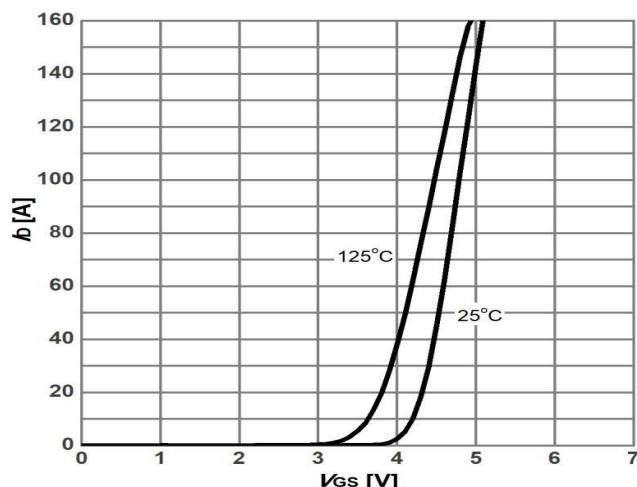
**Note:**

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is  $V_{\text{DD}}= 50\text{V}$ ,  $R_g=25\Omega$ ,  $L=0.5\text{mH}$

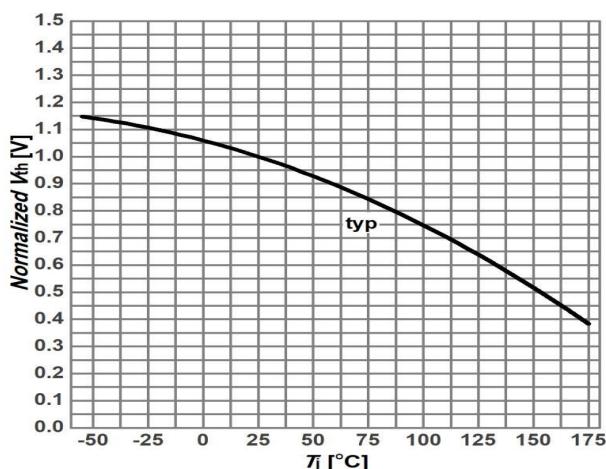
## Typical Characteristics



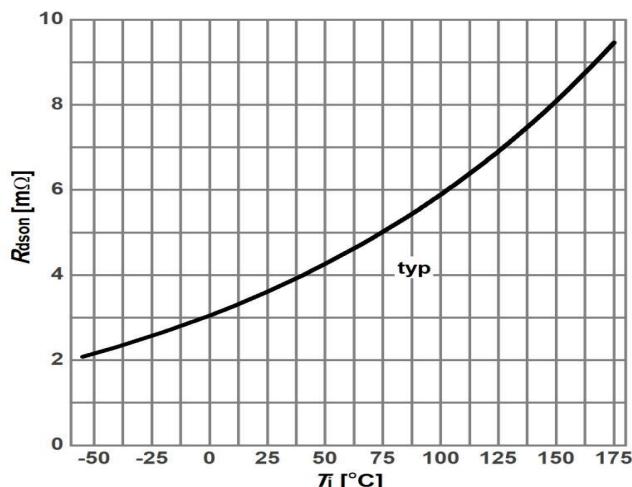
**Fig 1: Typ. output characteristics**



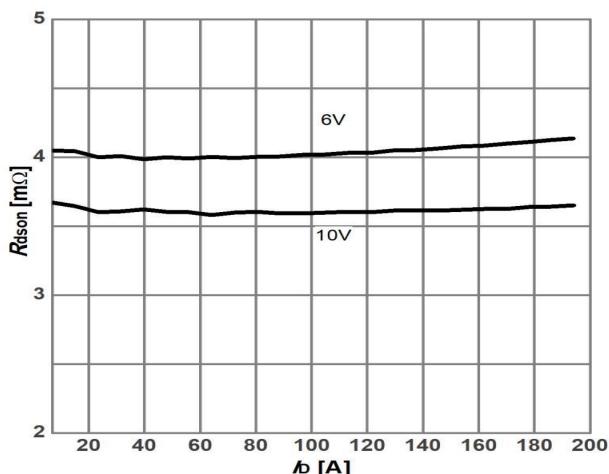
**Fig 2: Typ. transfer characteristics**



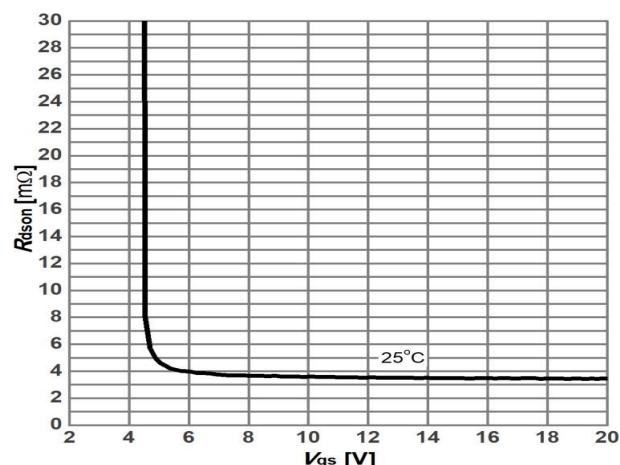
**Fig 3: Gate threshold voltage vs. Junction temperature**



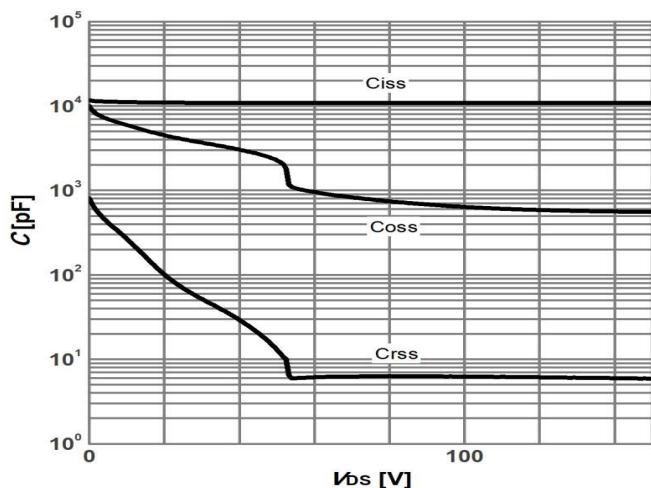
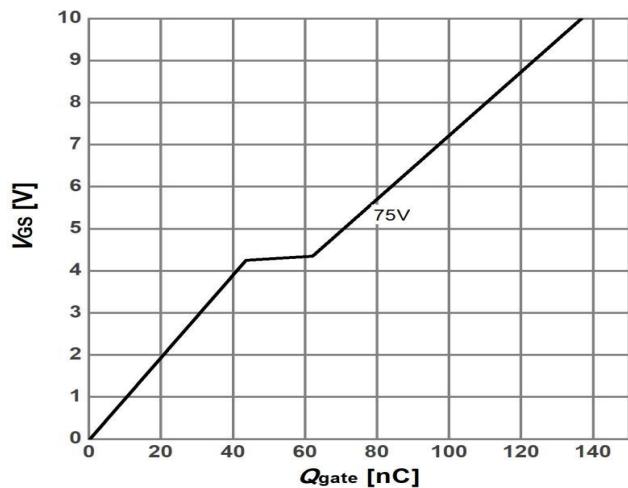
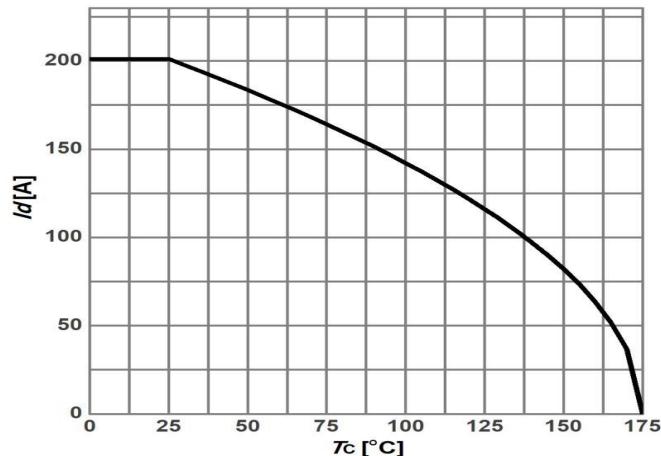
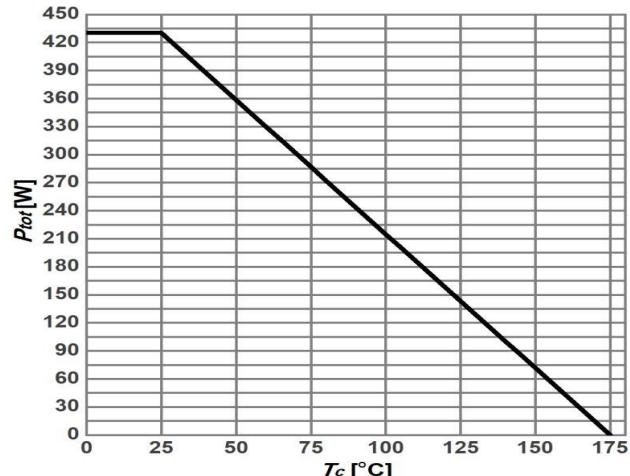
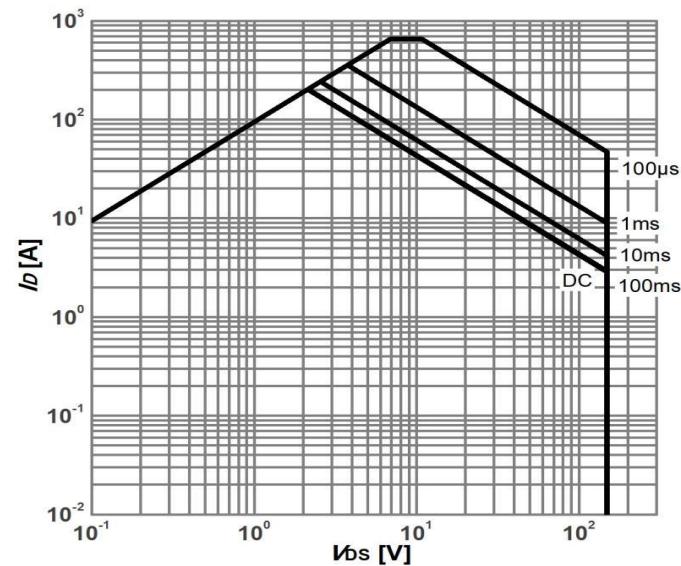
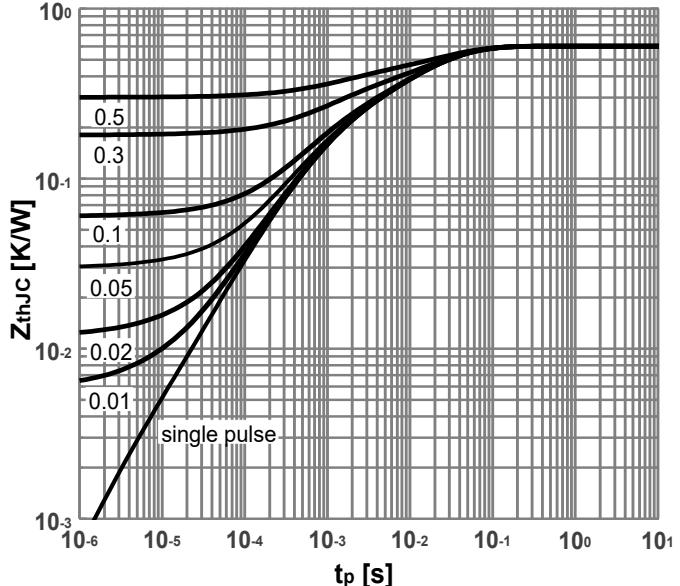
**Fig 4: On-state resistance vs. Junction temperature**



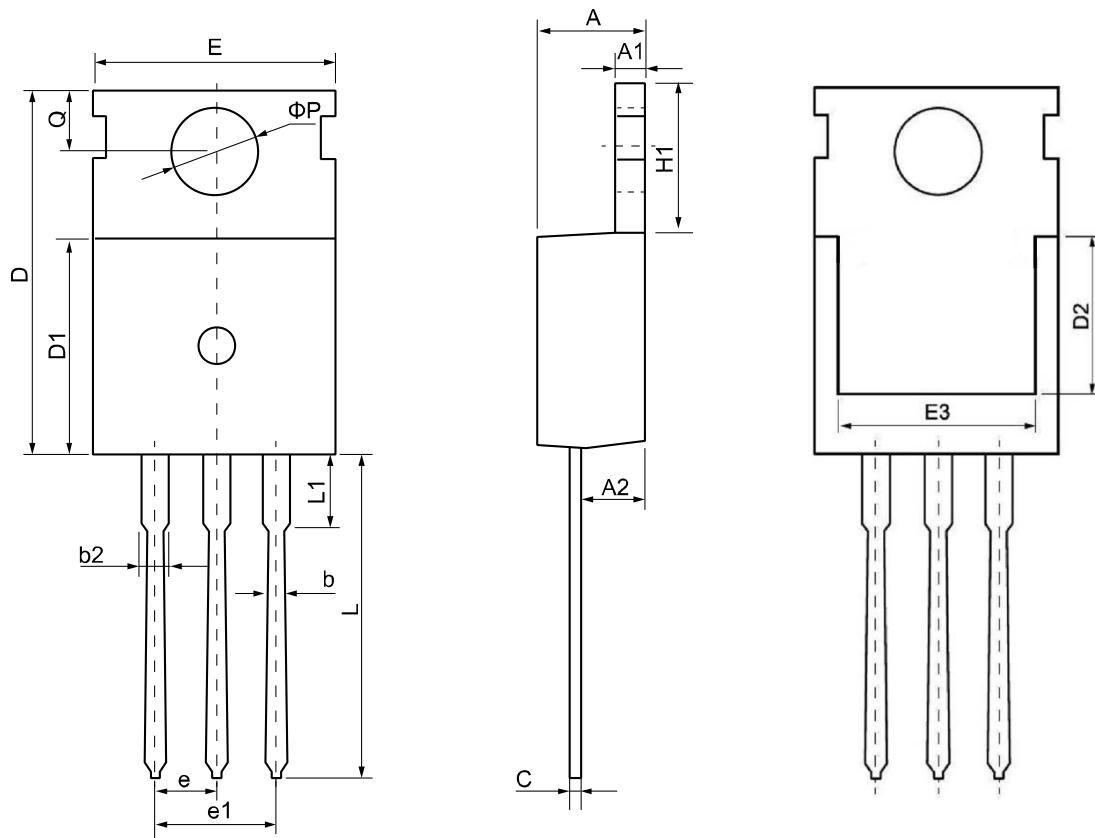
**Fig 5: On-state resistance vs. Drain current**



**Fig 6: On-state resistance vs.  $V_{gs}$  characteristics**


**Fig 7: Typ. capacitances**

**Fig 8: Typ. gate charge**

**Fig 9: Maximum Drain Current**

**Fig 10: Power dissipation**

**Fig 11: Safe operating area**

**Fig 12: Max. transient thermal impedance**

### TO-220 Package Outline Dimensions



<b>Symbol</b>	<b>Dimensions (unit:mm)</b>			<b>Symbol</b>	<b>Dimensions (unit:mm)</b>		
	<b>Min</b>	<b>Typ</b>	<b>Max</b>		<b>Min</b>	<b>Typ</b>	<b>Max</b>
<b>A</b>	4.30	4.55	4.75	<b>E</b>	9.65	10.00	10.25
<b>A1</b>	1.15	1.30	1.45	<b>E3</b>	7.00	--	--
<b>A2</b>	2.20	2.40	2.60	<b>e</b>	2.54 BSC		
<b>b</b>	0.70	0.80	0.95	<b>e1</b>	5.08 BSC		
<b>b2</b>	1.17	1.27	1.47	<b>H1</b>	6.30	6.50	6.80
<b>c</b>	0.40	0.50	0.65	<b>L</b>	12.70	13.50	14.10
<b>D</b>	15.30	15.60	15.90	<b>L1</b>	--	3.20	3.95
<b>D1</b>	8.90	9.10	9.35	<b>φP</b>	3.40	3.60	3.80
<b>D2</b>	5.50	--	--	<b>Q</b>	2.60	2.80	3.00



## **Printing Information**

**ATC** =====Brand

**XXXXXXX** =====Material Code

**XXYY** =====XX Representative Year  
YY Representative Weeks