

Features

- Advanced Shield Gate Trench technology
- Super Low Gate Charge
- High-Speed Switching
- 100% EAS Guaranteed
- Green Device Available

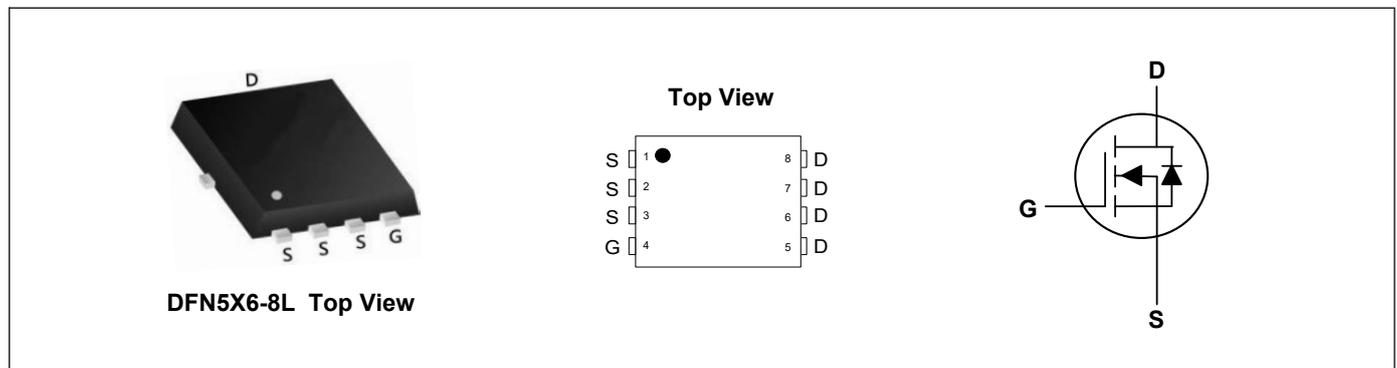
Applications

- High Frequency Point-of-Load, Synchronous Buck Converter
- Networking DC-DC Power System
- Load Switch

Product Summary



V_{DS}	100	V
I_D	129	A
$R_{DS(ON)}$ (at $V_{GS}=10V$)	4.0	m Ω
$R_{DS(ON)}$ (at $V_{GS}=4.5V$)	6.0	m Ω



Absolute Maximum Ratings ($T_C=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Rating	Units
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ¹	I_D	129	A
Continuous Drain Current ¹	I_D	80	A
$T_C=100^\circ\text{C}$			
Pulsed Drain Current ²	I_{DM}	402	A
Single Pulse Avalanche Energy ³	E_{AS}	101	mJ
Total Power Dissipation ⁴	P_D	136	W
Storage Temperature Range	T_{STG}	-55 to 150	$^\circ\text{C}$
Operating Junction Temperature Range	T_J	-55 to 150	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance Junction-Ambient ¹	$R_{\theta JA}$	---	49	$^\circ\text{C/W}$
Thermal Resistance Junction-Case ¹	$R_{\theta JC}$	---	0.92	$^\circ\text{C/W}$

Electrical Characteristics ($T_J=25^{\circ}\text{C}$, unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	100	---	---	V
Static Drain-Source On-Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=20A$	---	3.3	4.0	$m\Omega$
		$V_{GS}=4.5V, I_D=15A$	---	4.8	6.0	$m\Omega$
Gate Threshold Voltage	$V_{GS(th)}$	$V_{GS}=V_{DS}, I_D=250\mu A$	1.2	1.8	2.4	V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=100V, V_{GS}=0V$	---	---	1	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA
Forward Transconductance	g_{fs}	$V_{DS}=5V, I_D=20A$	---	81	---	S
Total Gate Charge	Q_g	$V_{DS}=50V, V_{GS}=10V, I_D=20A$	---	91	---	nC
Gate-Source Charge	Q_{gs}		---	7.9	---	
Gate-Drain Charge	Q_{gd}		---	31.5	---	
Turn-On Delay Time	$T_{d(on)}$	$V_{DS}=50V, V_{GS}=10V, R_G=10\Omega, I_D=20A$	---	15.3	---	ns
Rise Time	T_r		---	17.8	---	
Turn-Off Delay Time	$T_{d(off)}$		---	52.4	---	
Fall Time	T_f		---	23.6	---	
Input Capacitance	C_{iss}	$V_{DS}=50V, V_{GS}=0V, f=1MHz$	---	3957	---	pF
Output Capacitance	C_{oss}		---	920	---	
Reverse Transfer Capacitance	C_{rss}		---	41	---	

Drain-Source Diode Characteristics

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Diode Forward Voltage ²	V_{SD}	$V_{GS}=0V, I_F=20A, T_J=25^{\circ}\text{C}$	---	---	1.2	V
Reverse Recovery Time	t_{rr}	$I_F=20A$	---	44	---	nS
Reverse Recovery Charge	Q_{rr}	$di/dt=500A/\mu s, T_J=25^{\circ}\text{C}$	---	212	---	nC

Note:

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is $V_{DD}=25V, R_G=25\Omega, L=0.1mH$

Typical Characteristics

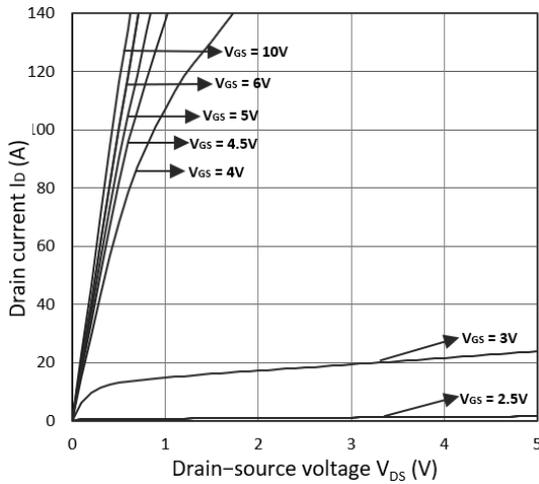


Figure 1. Output Characteristics

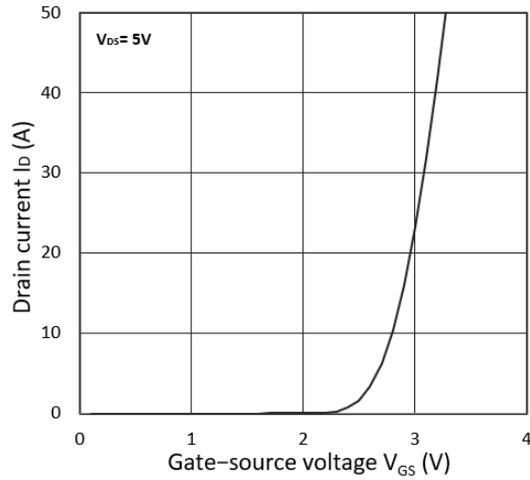


Figure 2. Transfer Characteristics

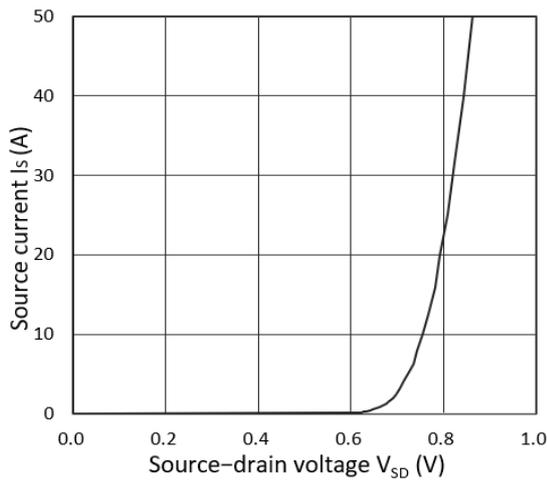


Figure 3. Forward Characteristics of Reverse

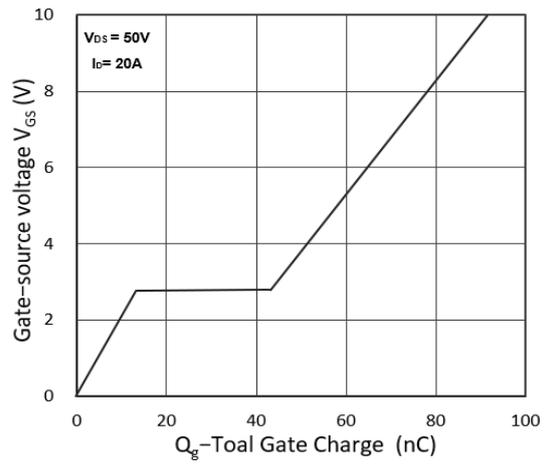


Figure 4. Gate Charge Characteristics

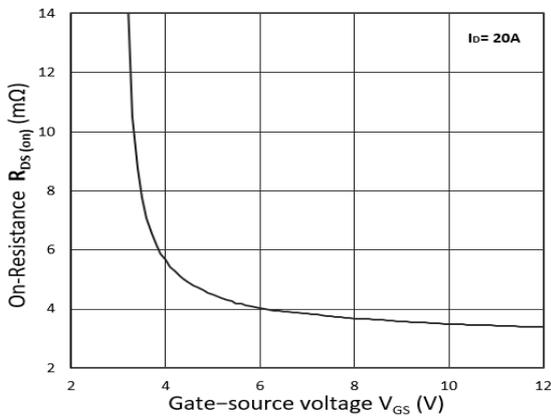


Figure 5. $R_{DS(on)}$ vs. V_{GS}

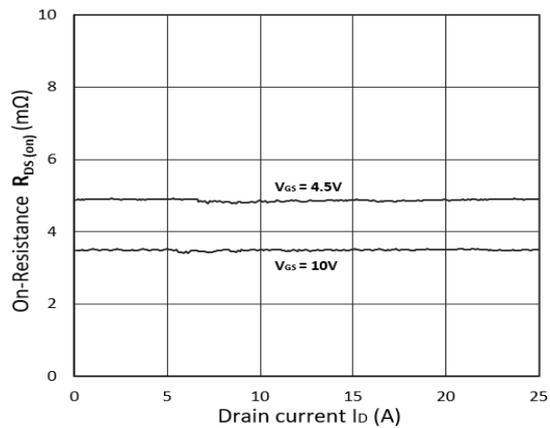


Figure 6. $R_{DS(on)}$ vs. I_D

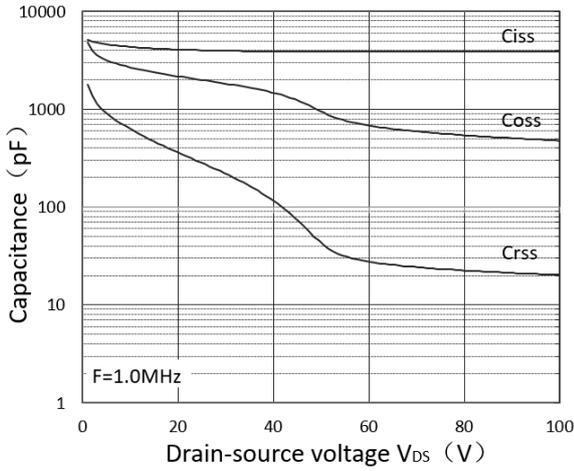


Figure 7. Capacitance Characteristics

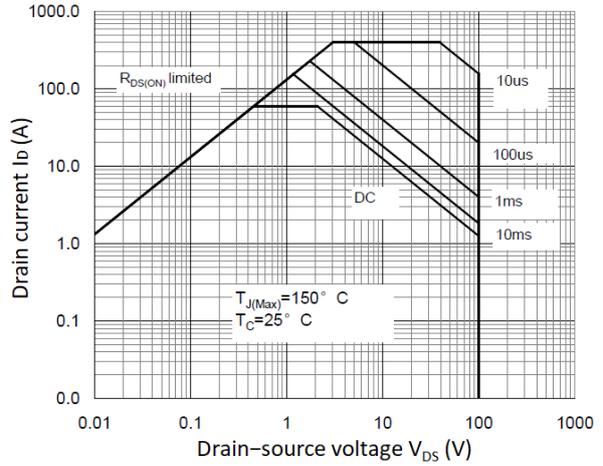


Figure 8. Safe Operating Area

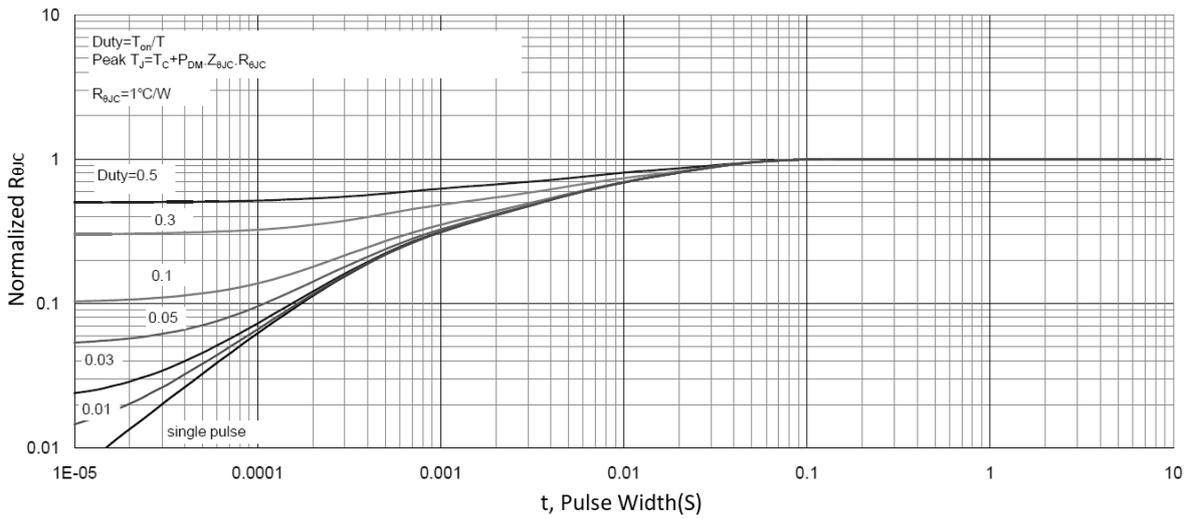
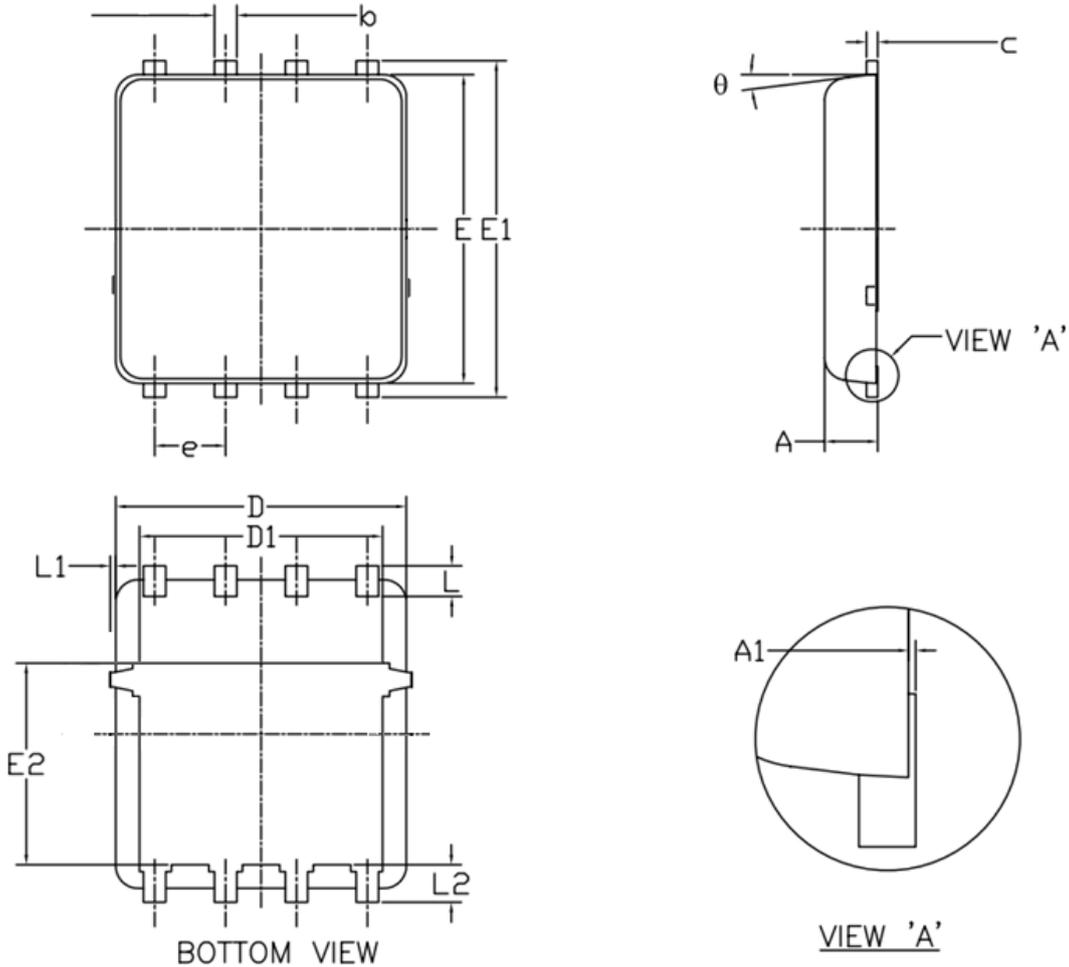


Figure 9. Normalized Maximum Transient Thermal Impedance

DFN5X6-8L Package Outline Dimensions



Symbol	Dimensions (unit:mm)			Symbol	Dimensions (unit:mm)		
	Min	Typ	Max		Min	Typ	Max
A	0.90	1.00	1.20	E1	5.90	6.10	6.35
A1	0.00	--	0.05	E2	3.38	3.58	3.92
b	0.30	0.40	0.51	e	1.27 BSC		
c	0.20	0.25	0.33	L	0.51	0.61	0.71
D	4.80	4.90	5.40	L1	--	--	0.15
D1	3.61	4.00	4.25	L2	0.41	0.51	0.61
E	5.65	5.80	6.06	theta	0°	--	12°