

## Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Green Device Available

## Product Summary



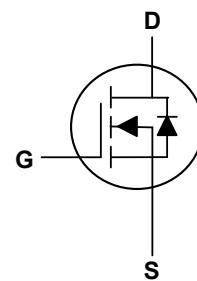
$V_{DS}$	60	V
$I_D$	5	A
$R_{DS(ON)}$ (at $V_{GS}=10V$ )	105	mΩ
$R_{DS(ON)}$ (at $V_{GS}=4.5V$ )	120	mΩ

## Applications

- High Frequency Point-of-Load,Synchronous Buck Converter
- Networking DC-DC Power System
- Load Switch



SOT89 Top View



## Absolute Maximum Ratings( $T_A=25^\circ C$ , unless otherwise noted)

Parameter	Symbol	Rating	Units
Drain-Source Voltage	$V_{DS}$	60	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current <sup>1</sup>	$I_D @ T_c = 25^\circ C$	5	A
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	10	A
Single Pulse Avalanche Energy <sup>3</sup>	$E_{AS}$	16	mJ
Total Power Dissipation <sup>4</sup>	$P_D$	1.7	W
Storage Temperature Range	$T_{STG}$	-55 to 150	°C
Operating Junction Temperature Range	$T_J$	-55 to 150	°C

## Thermal Characteristics

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance Junction-Ambient <sup>1</sup>	$R_{\theta JA}$	---	73	°C/W
Thermal Resistance Junction-Case <sup>1</sup>	$R_{\theta JC}$	---	10	°C/W

**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$	60	---	---	V
Static Drain-Source On-Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}$ , $I_D=3\text{A}$	---	70	105	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$ , $I_D=3\text{A}$	---	90	120	$\text{m}\Omega$
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D = 250\mu\text{A}$	1.0	---	2.0	V
Drain-Source Leakage Current	$I_{\text{DSS}}$	$V_{\text{DS}}=60\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	1	$\mu\text{A}$
Gate-Source Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA
Total Gate Charge	$Q_g$	$V_{\text{DS}}=30\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $I_D=3\text{A}$	---	12	---	nC
Gate-Source Charge	$Q_{\text{gs}}$		---	1.6	---	
Gate-Drain Charge	$Q_{\text{gd}}$		---	3	---	
Turn-On Delay Time	$T_{\text{d}(\text{on})}$	$V_{\text{DS}}=30\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $R_G=1\Omega$ , $I_D=3\text{A}$	---	6	---	ns
Rise Time	$T_r$		---	15	---	
Turn-Off Delay Time	$T_{\text{d}(\text{off})}$		---	15	---	
Fall Time	$T_f$		---	10	---	
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=30\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	515	---	pF
Output Capacitance	$C_{\text{oss}}$		---	34	---	
Reverse Transfer Capacitance	$C_{\text{rss}}$		---	26	---	

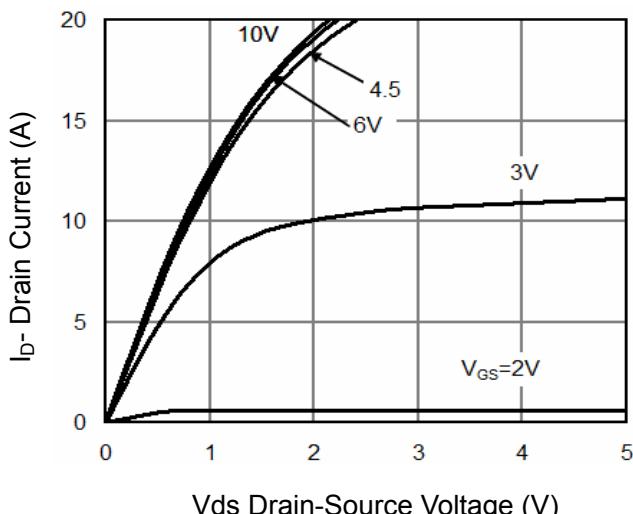
**Drain-Source Diode Characteristics**

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Diode Forward Voltage <sup>2</sup>	$V_{\text{SD}}$	$V_{\text{GS}}=0\text{V}$ , $I_S=3\text{A}$ , $T_J=25^\circ\text{C}$	---	---	1.2	V

**Note:**

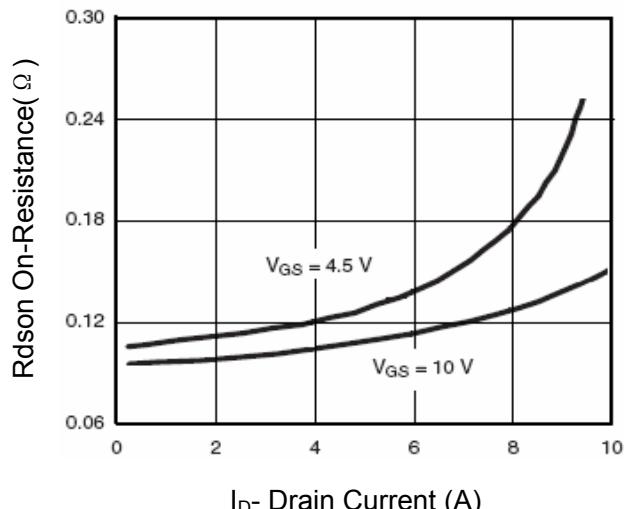
1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
2. The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$
3. The EAS data shows Max. rating . The test condition is  $V_{\text{DD}}=30\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $L=0.5\text{mH}$
4. The power dissipation is limited by  $175^\circ\text{C}$  junction temperature
5. The data is theoretically the same as  $I_D$  and  $I_{\text{DM}}$  , in real applications , should be limited by total power dissipation.

## Typical Characteristics



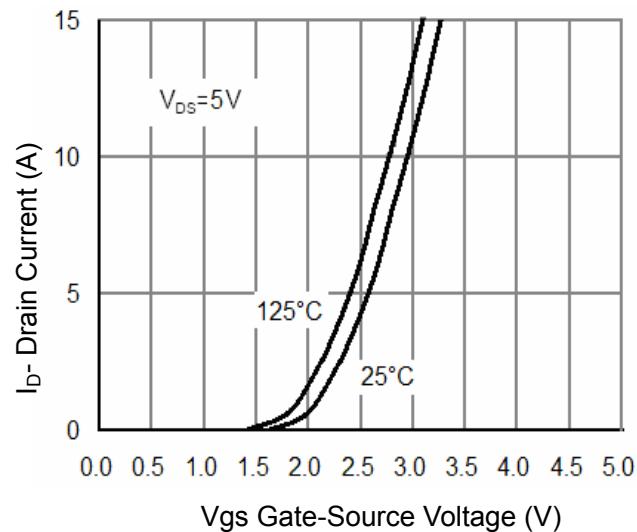
V<sub>DS</sub> Drain-Source Voltage (V)

**Figure 1 Output Characteristics**



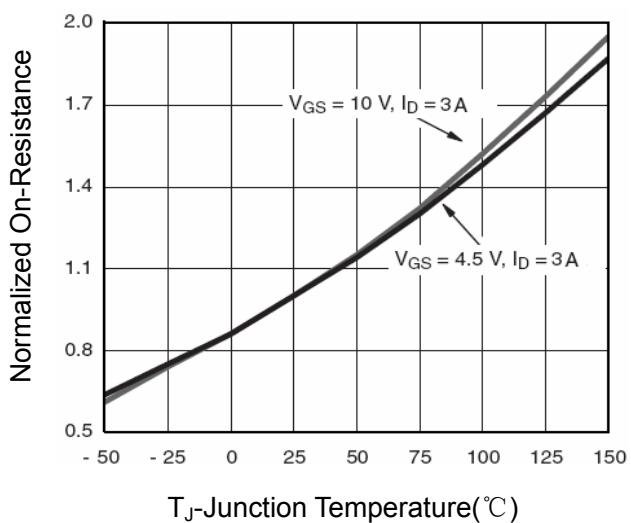
I<sub>D</sub> Drain Current (A)

**Figure 2 Drain-Source On-Resistance**



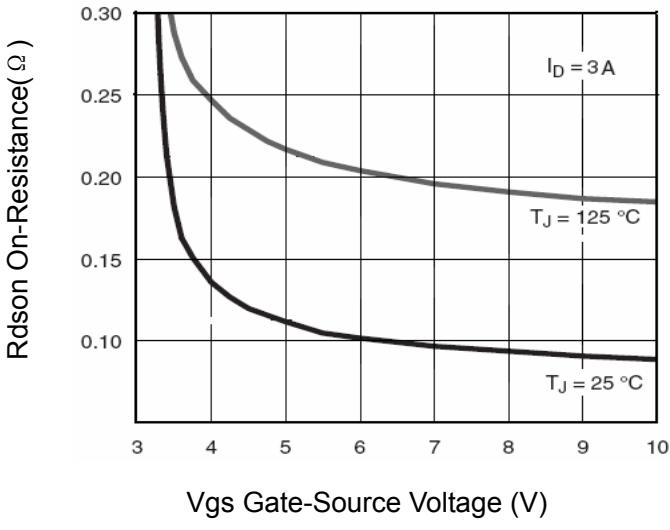
V<sub>DS</sub> Gate-Source Voltage (V)

**Figure 3 Transfer Characteristics**



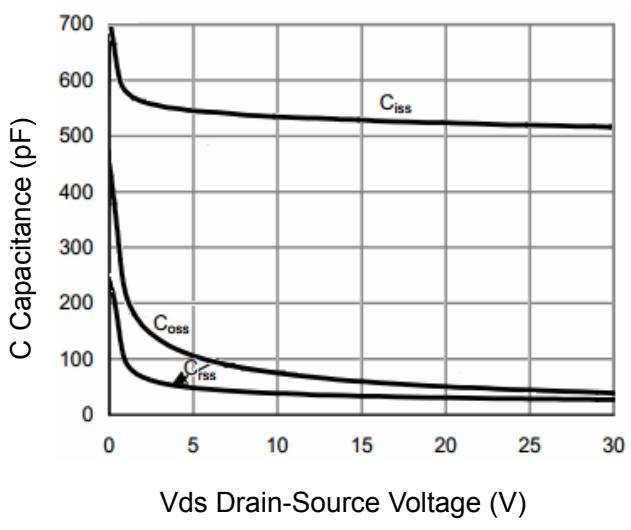
T<sub>J</sub> Junction Temperature (°C)

**Figure 4 Drain-Source On-Resistance**



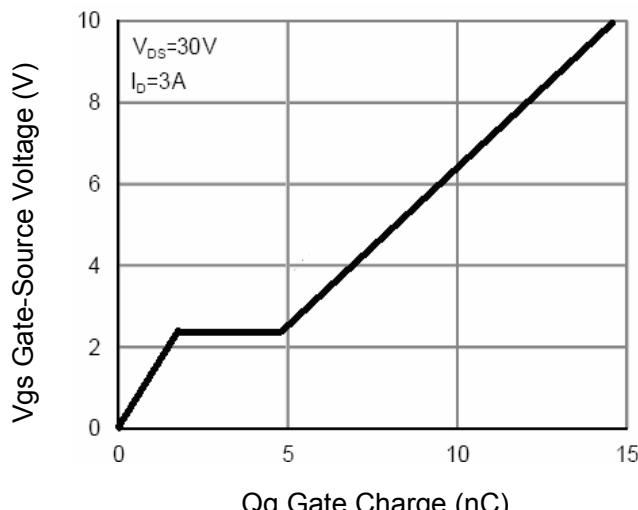
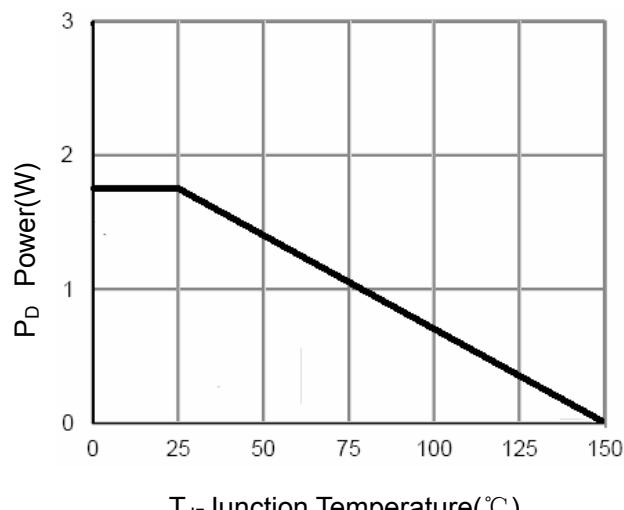
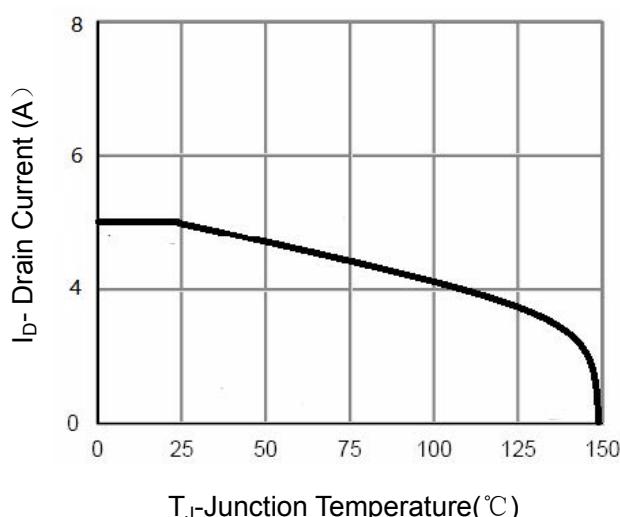
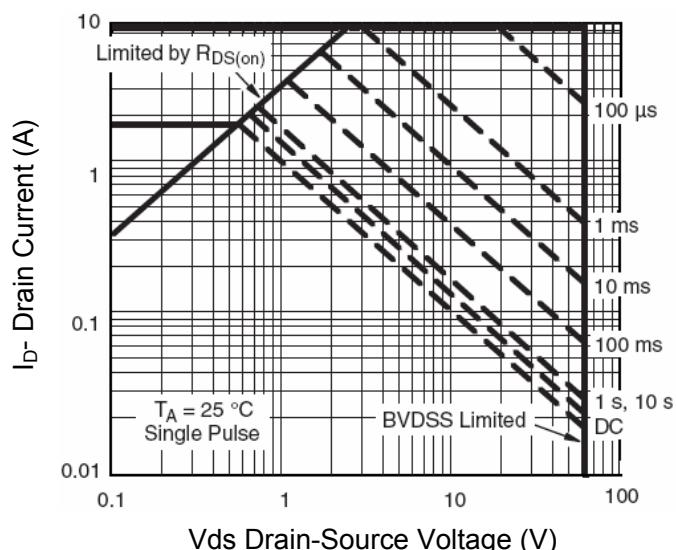
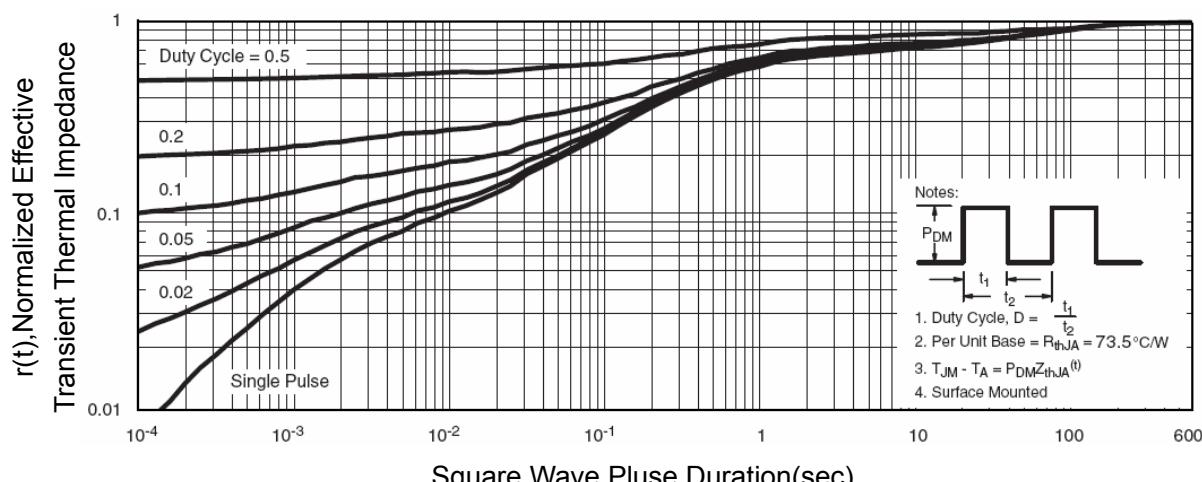
V<sub>DS</sub> Gate-Source Voltage (V)

**Figure 5 Rdson vs Vgs**

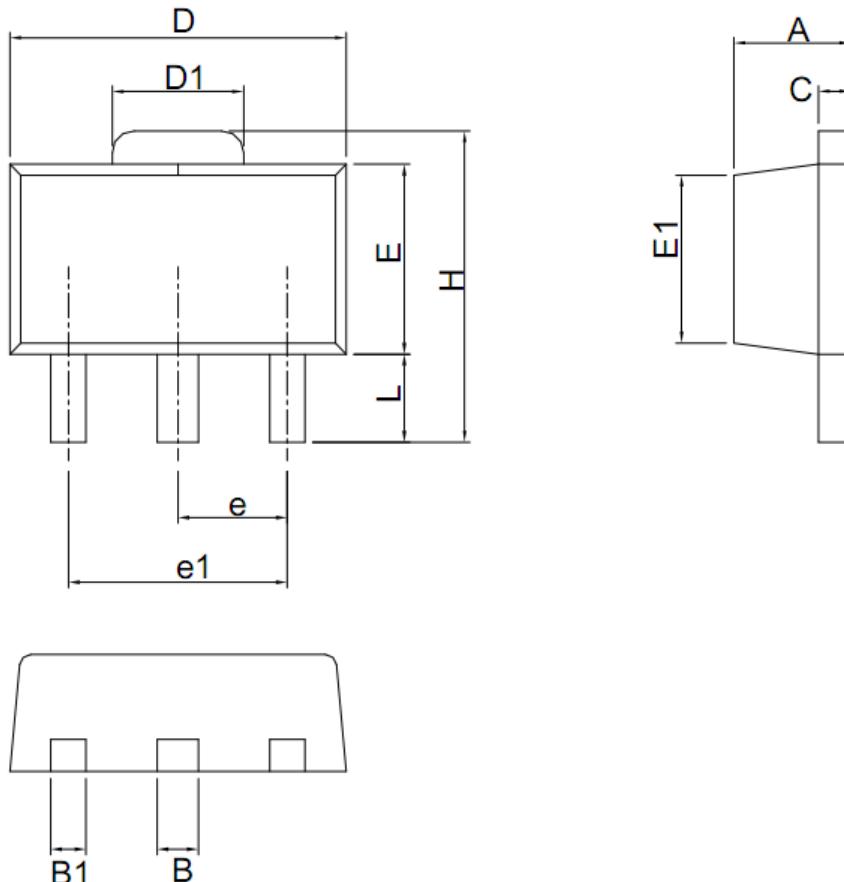


V<sub>DS</sub> Drain-Source Voltage (V)

**Figure 6 Capacitance vs Vds**


**Figure 7 Gate Charge**

**Figure 8 Power Dissipation**

**Figure 9 Drain Current**

**Figure 10 Safe Operation Area**

**Figure 11 Normalized Maximum Transient Thermal Impedance**

### SOT89 Package Outline Dimensions



Symbol	Dimensions (unit:mm)			Symbol	Dimensions (unit:mm)		
	Min	Typ	Max		Min	Typ	Max
A	1.40	1.50	1.60	E	2.40	2.50	2.60
B	0.40	0.50	0.56	E1	2.10	2.20	2.30
B1	0.32	0.40	0.50	e	1.50 BSC		
C	0.35	0.40	0.44	e1	3.00 BSC		
D	4.40	4.50	4.60	H	3.94	4.10	4.25
D1	1.40	1.60	1.80	L	0.85	1.00	1.20