

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

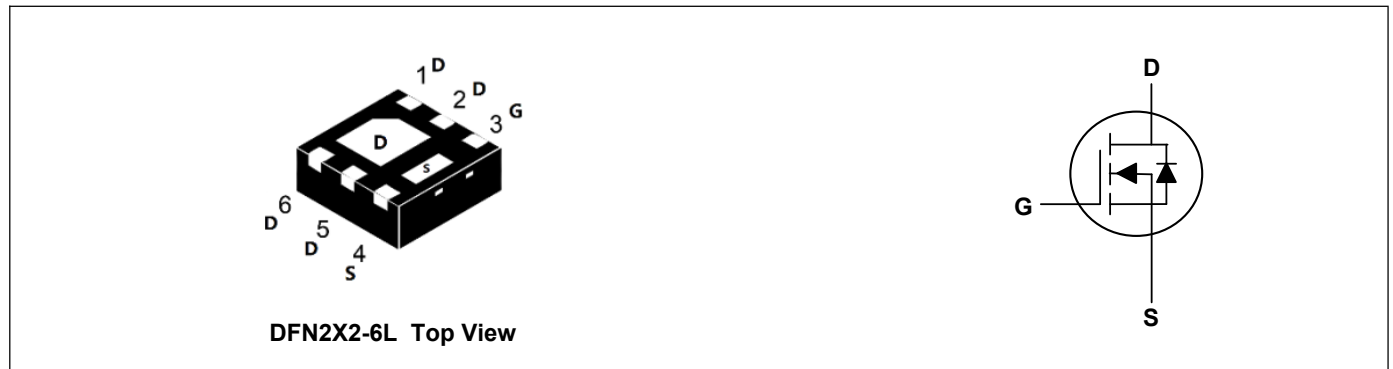
Product Summary



V_{DS}	40	V
I_D	30	A
$R_{DS(ON)}$ Typ (at $V_{GS}=10V$)	11	m Ω
$R_{DS(ON)}$ Typ (at $V_{GS}=4.5V$)	17	m Ω

Applications

- High Frequency Point-of-Load, Synchronous Buck Converter
- Networking DC-DC Power System
- Load Switch



Absolute Maximum Ratings ($T_C=25^{\circ}C$, unless otherwise noted)

Parameter	Symbol	Rating	Units
Drain-Source Voltage	V_{DS}	40	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ¹	$I_D@T_A=25^{\circ}C$	30	A
Continuous Drain Current ¹	$I_D@T_A=70^{\circ}C$	24	A
Pulsed Drain Current ²	I_{DM}	120	A
Single Pulse Avalanche Energy ³	E_{AS}	29	mJ
Total Power Dissipation ⁴	P_D	26	W
Storage Temperature Range	T_{STG}	-55 to 150	$^{\circ}C$
Operating Junction Temperature Range	T_J	-55 to 150	$^{\circ}C$

Thermal Characteristics

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance Junction-Case ¹	$R_{\theta JC}$	---	4.8	$^{\circ}C/W$

Electrical Characteristics (T_J=25°C, unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	40	---	---	V
Static Drain-Source On-Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =4A	---	11	13	mΩ
		V _{GS} =4.5V, I _D =3A	---	17	22	mΩ
Gate Threshold Voltage	V _{GS(th)}	V _{GS} =V _{DS} , I _D =250uA	1.5	---	2.5	V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =40V, V _{GS} =0V, T _J =25°C	---	---	1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA
Total Gate Charge	Q _g	V _{DS} =20V, V _{GS} =10V, I _D =15A	---	7	---	nC
Gate-Source Charge	Q _{gs}		---	1.5	---	
Gate-Drain Charge	Q _{gd}		---	3	---	
Turn-On Delay Time	T _{d(on)}	V _{DS} =20V, V _{GS} =10V, R _G =3.3Ω, I _D =1A	---	7	---	ns
Rise Time	T _r		---	11	---	
Turn-Off Delay Time	T _{d(off)}		---	24	---	
Fall Time	T _f		---	17	---	
Input Capacitance	C _{iss}	V _{DS} =20V, V _{GS} =0V, f=1MHz	---	407	---	pF
Output Capacitance	C _{oss}		---	196	---	
Reverse Transfer Capacitance	C _{rss}		---	7.7	---	

Drain-Source Diode Characteristics

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Diode Forward Voltage ²	V _{SD}	V _{GS} =0V, I _S =40A, T _J =25°C	---	---	1.2	V

Note:

1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
2. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%

Typical Characteristics

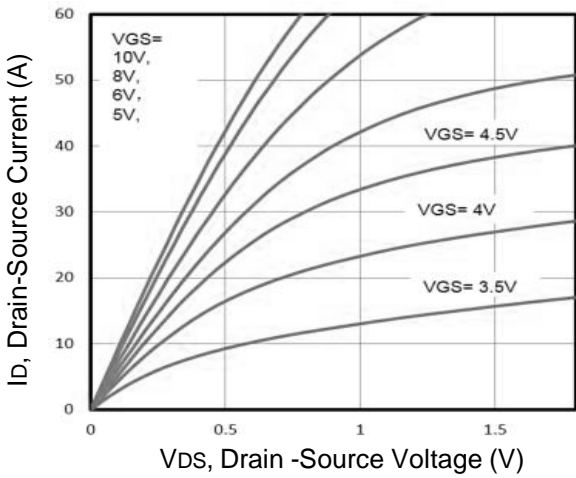


Fig1. Typical Output Characteristics

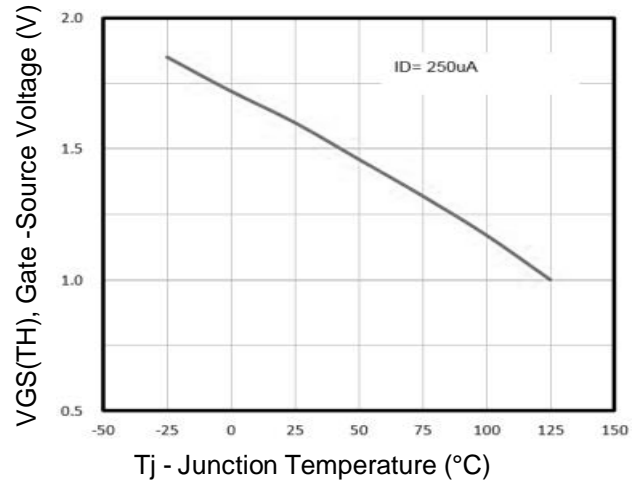


Fig2. Normalized Threshold Voltage Vs. Temperature

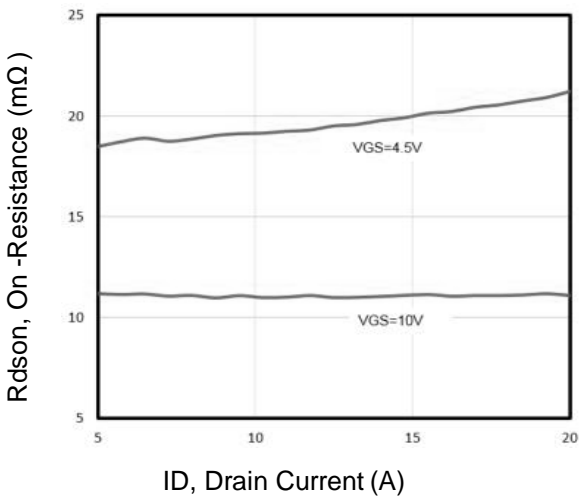


Fig3. On-Resistance vs. Drain Current and Gate

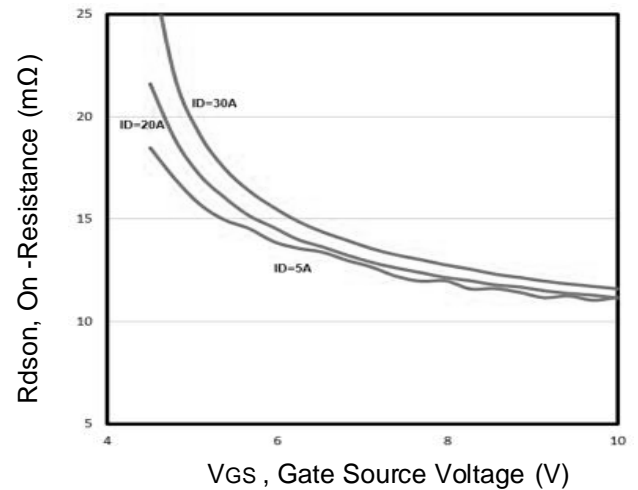


Fig4. On-Resistance vs. Gate Source Voltage

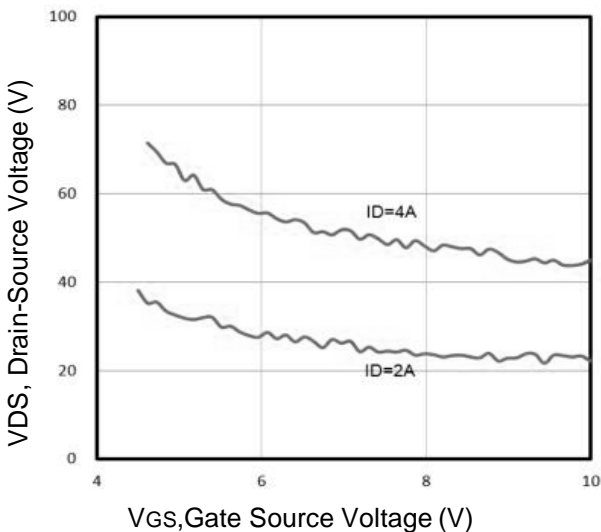


Fig5. Drain-Source Voltage vs Gate-Source Voltage

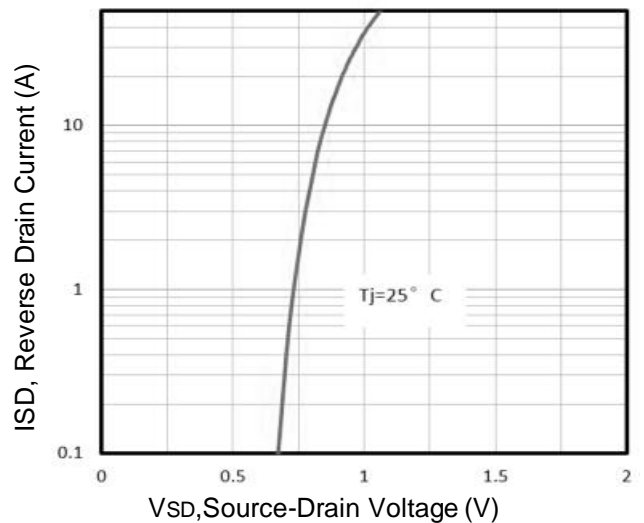
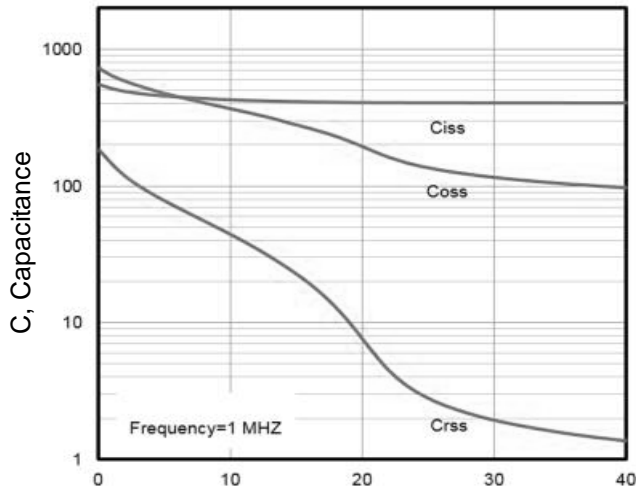
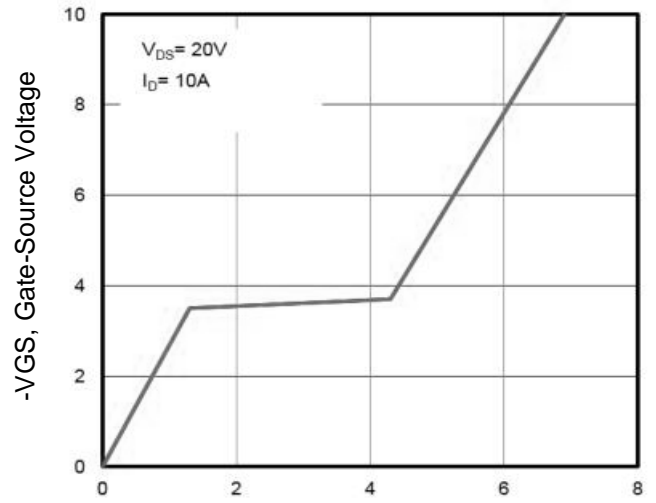


Fig6. Typical Source-Drain Diode Forward Voltage



V_{DS}, Drain-Source Voltage (V)

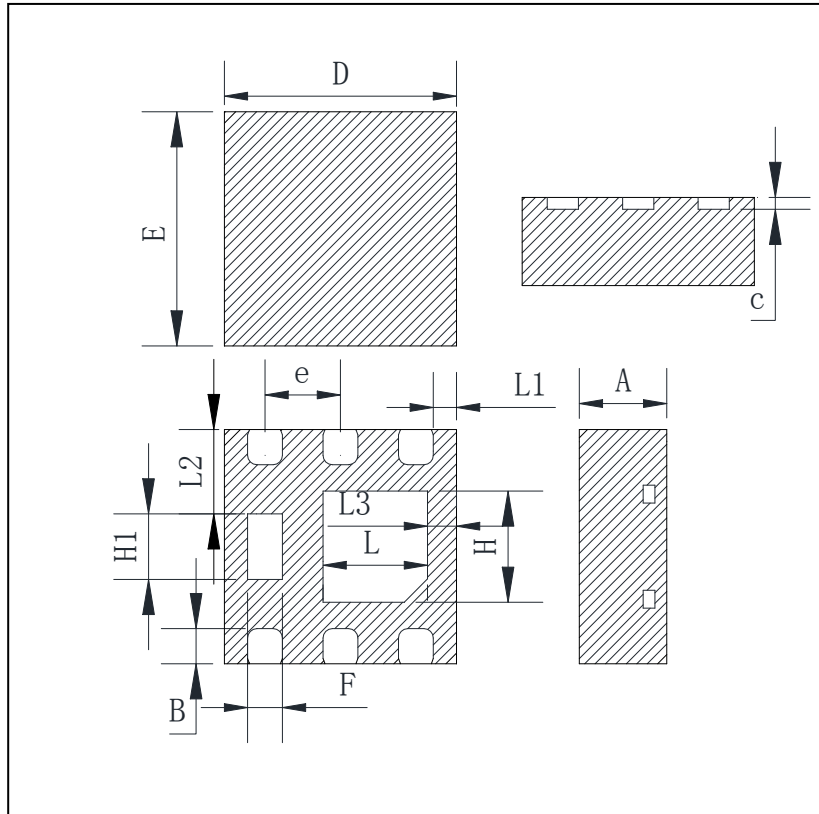
Fig7. Typical Capacitance Vs. Drain-Source Voltage



Q_g, Total Gate Charge (nC)

Fig8. Typical Gate Charge Vs. Gate-Source Voltage

DFN2X2-6L Package Outline Data



Symbol	Min	Typ	Max
A	0.70	0.75	0.80
B	0.25	0.30	0.35
C	0.153	0.203	0.253
D	1.90	2.00	2.10
E	1.90	2.00	2.10
e	0.60	0.65	0.70
F	0.25	0.30	0.35
H	0.90	1.00	1.10
H1	0.50	0.60	0.65
L	0.80	0.90	1.00
L1	0.15	0.20	0.25
L2	0.60	0.70	0.80
L3	0.25	0.30	0.35

UNIT: mm