

## Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

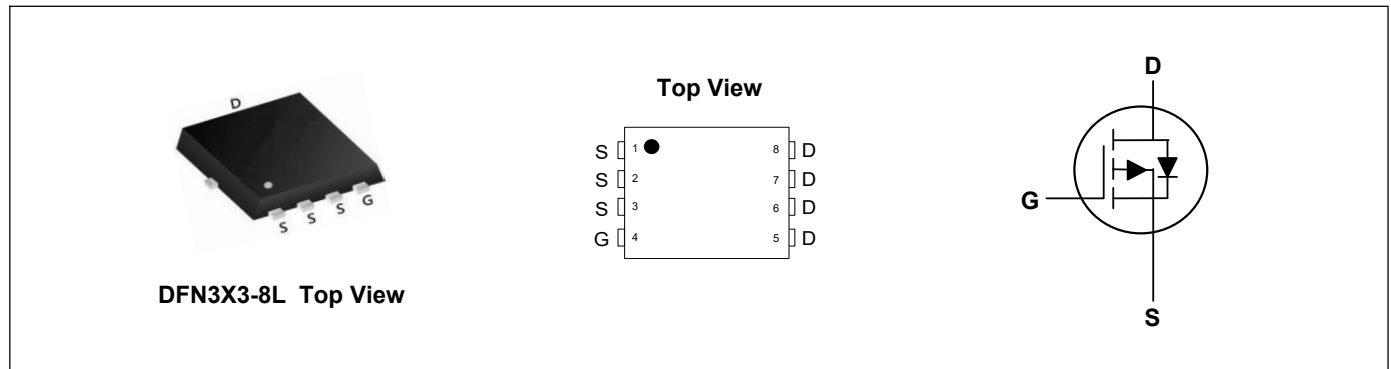
## Applications

- High Frequency Point-of-Load, Synchronous Buck Converter
- Networking DC-DC Power System
- Load Switch, LED applications

## Product Summary



$V_{DS}$	-30	V
$I_D$	-50	A
$R_{DS(ON)}$ (at $V_{GS}=-10V$ )	8.7	m $\Omega$
$R_{DS(ON)}$ (at $V_{GS}=-4.5V$ )	13.5	m $\Omega$



## Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ , unless otherwise noted)

Parameter	Symbol	Rating	Units
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current, $V_{GS} @ -10V^1$	$I_D @ T_A=25^\circ\text{C}$	-50	A
Continuous Drain Current, $V_{GS} @ -10V^1$	$I_D @ T_A=70^\circ\text{C}$	-32	A
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	-200	A
Single Pulse Avalanche Energy <sup>3</sup>	EAS	125	mJ
Avalanche Current	$I_{AS}$	-50	A
Total Power Dissipation <sup>4</sup>	$P_D @ T_A=25^\circ\text{C}$	38	W
Storage Temperature Range	$T_{STG}$	-55 to 150	$^\circ\text{C}$
Operating Junction Temperature Range	$T_J$	-55 to 150	$^\circ\text{C}$

## Thermal Characteristics

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance Junction-Ambient <sup>1</sup>	$R_{\theta JA}$	---	65	$^\circ\text{C/W}$
Thermal Resistance Junction-Case <sup>1</sup>	$R_{\theta JC}$	---	2.3	$^\circ\text{C/W}$

**Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)**

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-30	---	---	V
Static Drain-Source On-Resistance <sup>2</sup>	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-30A	---	7.3	8.7	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-15A	---	11	13.5	mΩ
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =-250uA	-1.2	---	-2.5	V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =-24V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	-1	uA
		V <sub>DS</sub> =-24V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C	---	---	-5	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	---	---	±100	nA
Forward Transconductance	g <sub>fs</sub>	V <sub>DS</sub> =-5V, I <sub>D</sub> =-30A	---	25	---	S
Total Gate Charge (-4.5V)	Q <sub>g</sub>	V <sub>DS</sub> =-15V, V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-15A	---	30	---	nC
Gate-Source Charge	Q <sub>gs</sub>		---	10	---	
Gate-Drain Charge	Q <sub>gd</sub>		---	10.4	---	
Turn-On Delay Time	T <sub>d(on)</sub>	V <sub>DS</sub> =-15V, V <sub>GS</sub> =-10V, R <sub>G</sub> =3.3Ω, I <sub>D</sub> =-15A	---	9.4	---	ns
Rise Time	T <sub>r</sub>		---	10.2	---	
Turn-Off Delay Time	T <sub>d(off)</sub>		---	117	---	
Fall Time	T <sub>f</sub>		---	24	---	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-15V, V <sub>GS</sub> =0V, f=1MHz	---	3448	---	pF
Output Capacitance	C <sub>oss</sub>		---	508	---	
Reverse Transfer Capacitance	C <sub>rss</sub>		---	421	---	

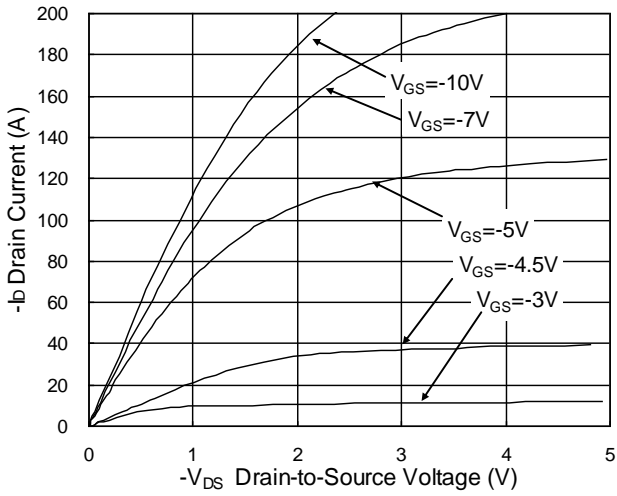
**Drain-Source Diode Characteristics**

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Continuous Source Current <sup>1,5</sup>	I <sub>S</sub>	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	-50	A
Pulsed Source Current <sup>2</sup>	I <sub>SM</sub>		---	---	-130	A
Diode Forward Voltage <sup>2</sup>	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =-1A, T <sub>J</sub> =25°C	---	---	-1	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =-15A, di/dt=100A/μs, T <sub>J</sub> =25°C	---	20	---	nS
Reverse Recovery Charge	Q <sub>rr</sub>		---	9.5	---	nC

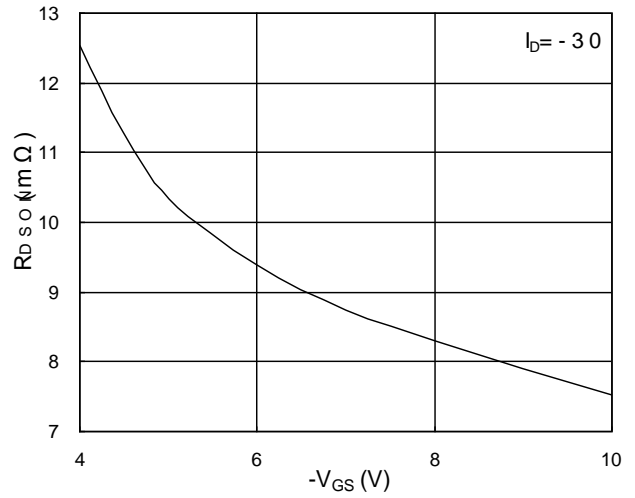
**Note:**

- The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
- The EAS data shows Max. rating. The test condition is V<sub>DD</sub>=-25V, V<sub>GS</sub>=-10V, L=0.1mH, I<sub>AS</sub>=-50A
- The power dissipation is limited by 150°C junction temperature
- The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub>, in real applications, should be limited by total power dissipation.

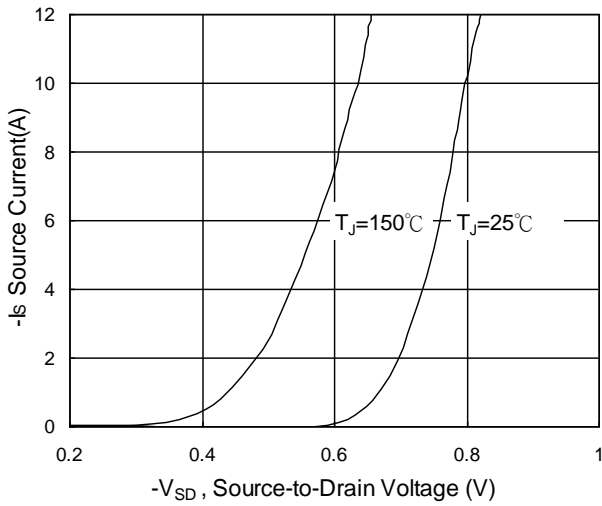
**Typical Characteristics**



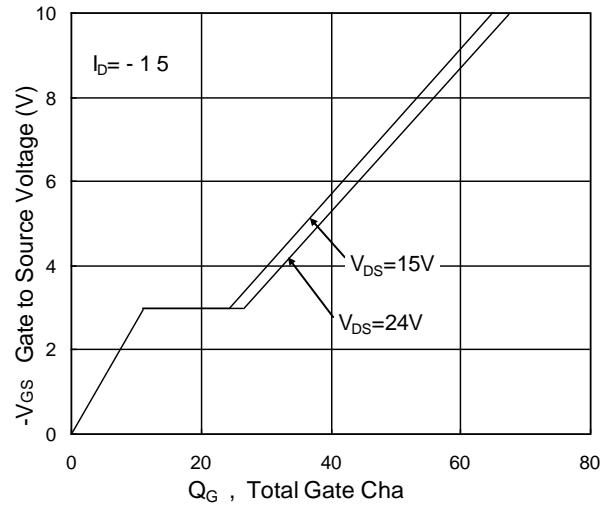
**Fig.1 Typical Output Characteristics**



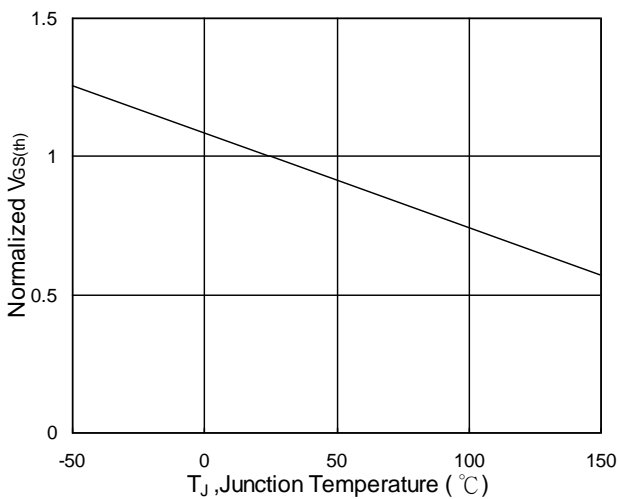
**Fig.2 On-Resistance v.s Gate-Source**



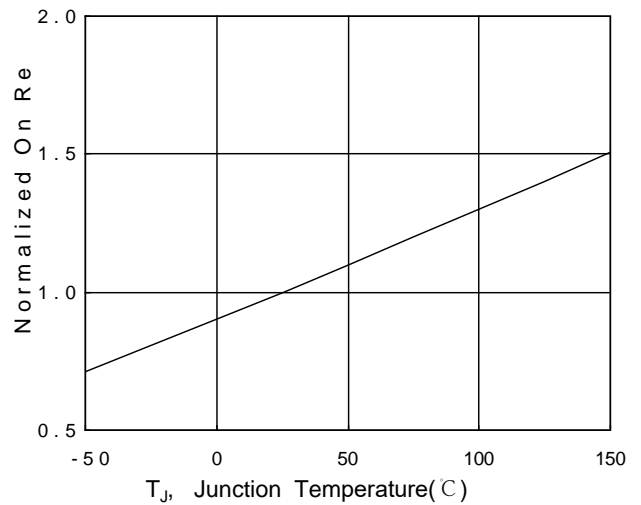
**Fig.3 Forward Characteristics Of Reverse**



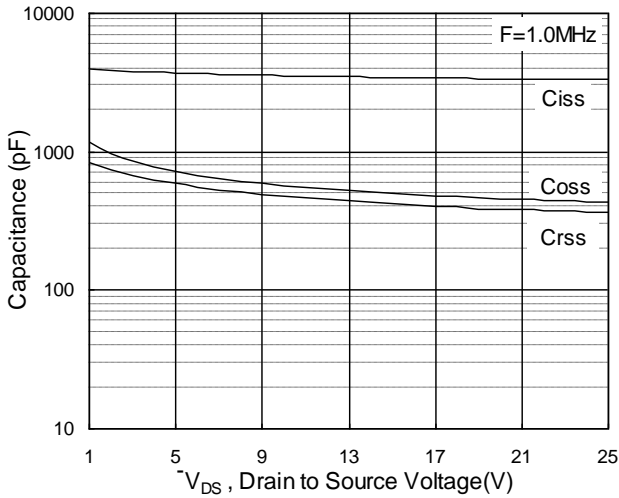
**Fig.4 Gate-Charge Characteristics**



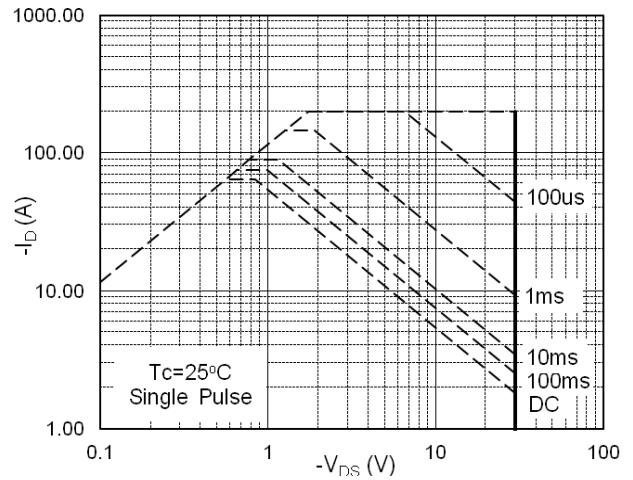
**Fig.5 Normalized  $V_{GS(th)}$  v.s  $T_J$**



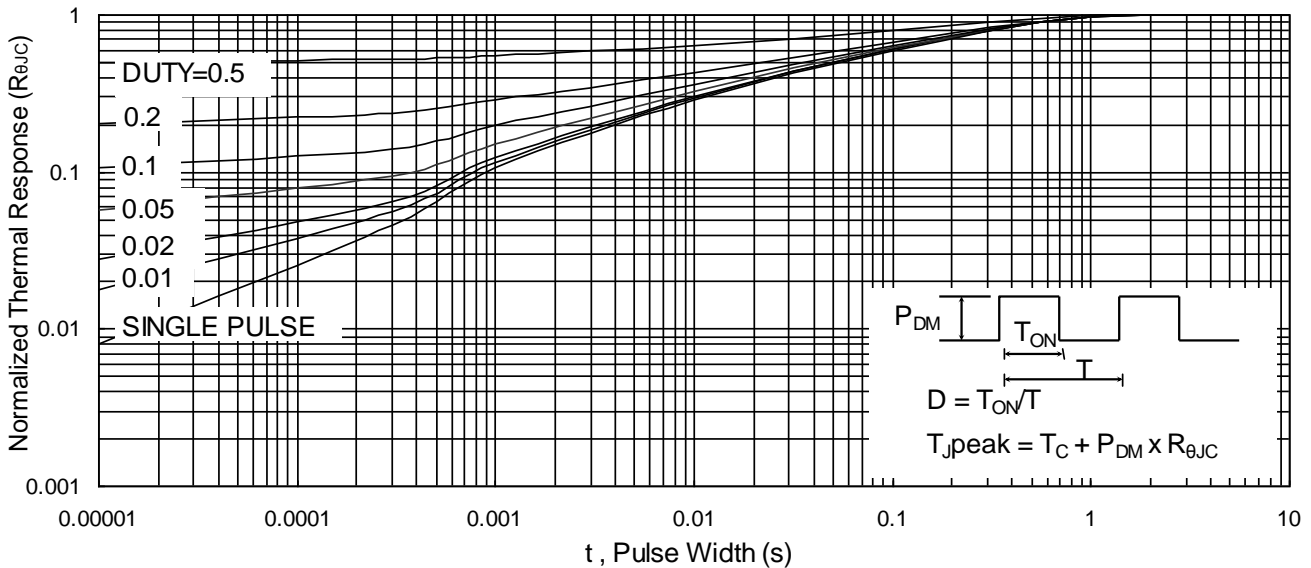
**Fig.6 Normalized  $R_{DS(on)}$  v.s  $T_J$**



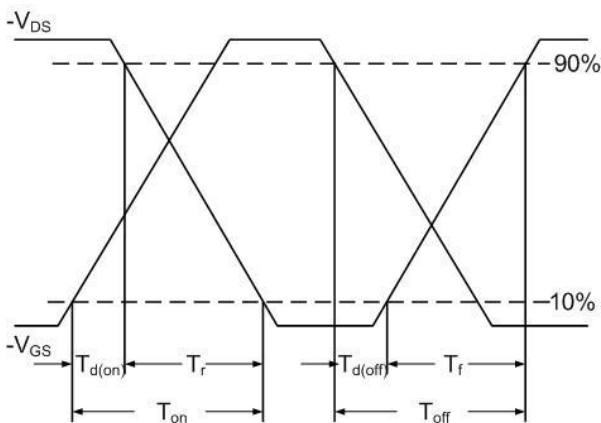
**Fig.7 Capacitance**



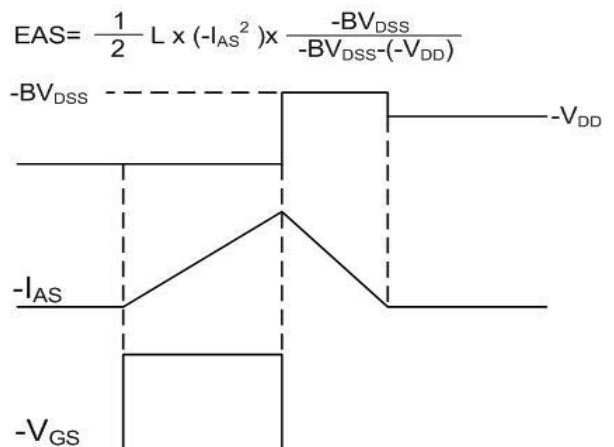
**Fig.8 Safe Operating Area**



**Fig.9 Normalized Maximum Transient Thermal Impedance**

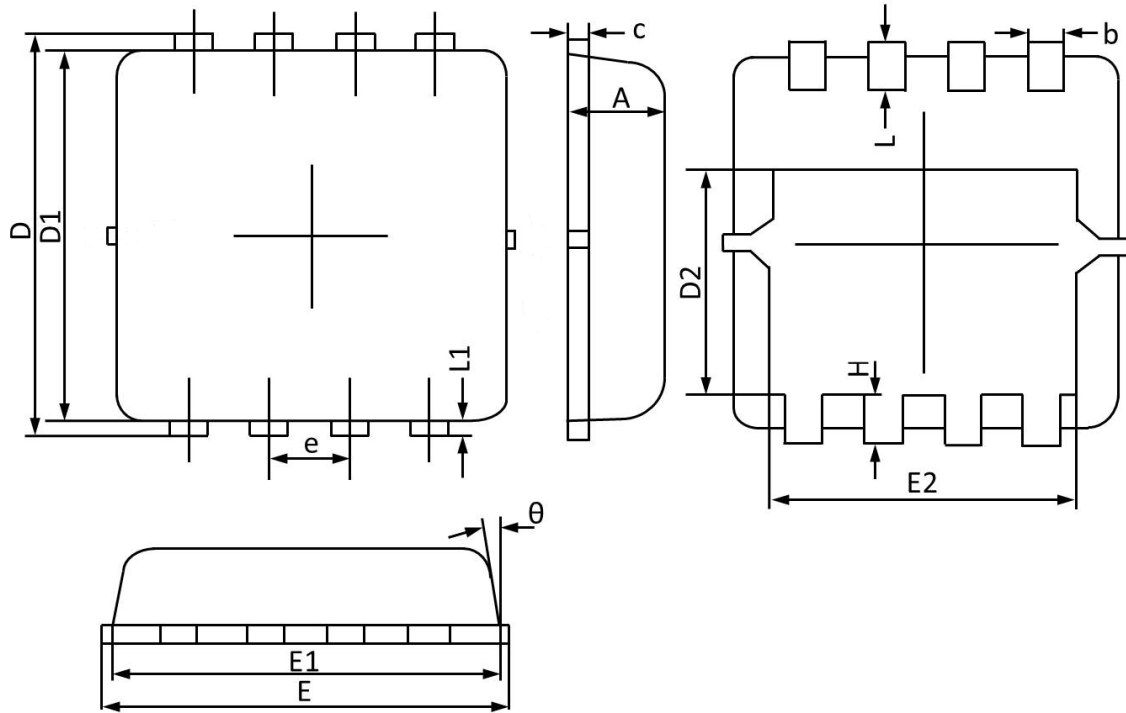


**Fig.10 Switching Time Waveform**



**Fig.11 Unclamped Inductive Switching Waveform**

**DFN3X3-8L Package Outline Dimensions**



Symbol	Dimensions (unit:mm)			Symbol	Dimensions (unit:mm)		
	Min	Typ	Max		Min	Typ	Max
<b>A</b>	0.70	0.75	0.85	<b>E1</b>	2.90	3.10	3.25
<b>b</b>	0.24	0.30	0.35	<b>E2</b>	2.35	2.50	2.60
<b>c</b>	0.10	0.17	0.25	<b>e</b>	0.65 BSC		
<b>D</b>	3.10	3.30	3.45	<b>H</b>	0.30	0.40	0.50
<b>D1</b>	2.90	3.05	3.20	<b>L</b>	0.30	0.40	0.50
<b>D2</b>	1.45	1.70	1.95	<b>L1</b>	--	0.13	--
<b>E</b>	3.05	3.25	3.40	<b>theta</b>	0°		14°