

## Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

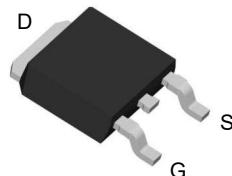
## Product Summary



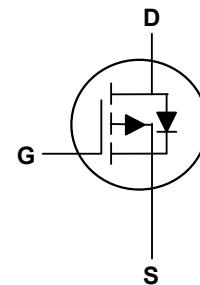
$V_{DS}$	-30	V
$I_D$	-122	A
$R_{DS(ON)}$ (at $V_{GS}=-10V$ )	4.3	mΩ
$R_{DS(ON)}$ (at $V_{GS}=-4.5V$ )	6.3	mΩ

## Applications

- High Frequency Point-of-Load,Synchronous Buck Converter
- Networking DC-DC Power System
- Load Switch



TO-252 Top View



## Absolute Maximum Ratings( $T_c=25^\circ\text{C}$ , unless otherwise noted)

Parameter	Symbol	Rating	Units
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current <sup>1</sup>	$I_D$	-122	A
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	-427	A
Single Pulse Avalanche Energy <sup>3</sup>	$E_{AS}$	196	mJ
Total Power Dissipation <sup>4</sup>	$P_D$	117	W
Storage Temperature Range	$T_{STG}$	-55 to 150	°C
Operating Junction Temperature Range	$T_J$	-55 to 150	°C

## Thermal Characteristics

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance Junction-Ambient <sup>1</sup>	$R_{\theta JA}$	---	62	°C/W
Thermal Resistance Junction-Case <sup>1</sup>	$R_{\theta JC}$	---	1.07	°C/W

**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-30	---	---	V
Static Drain-Source On-Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-20\text{A}$	---	3.3	4.3	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-20\text{A}$	---	4.7	6.3	$\text{m}\Omega$
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{GS}}=V_{\text{DS}}, I_{\text{D}}=-250\mu\text{A}$	-1.0	---	-2.5	V
Drain-Source Leakage Current	$I_{\text{DSS}}$	$V_{\text{DS}}=-30\text{V}, V_{\text{GS}}=0\text{V}$	---	---	-1	$\mu\text{A}$
Gate-Source Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm20\text{V}, V_{\text{DS}}=0\text{V}$	---	---	$\pm100$	nA
Gate Resistance	$R_g$	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	---	7	---	$\Omega$
Total Gate Charge	$Q_g$	$V_{\text{DS}}=-25\text{V}, V_{\text{GS}}=-10\text{V}, I_{\text{D}}=50\text{A}$	---	128	---	nC
Gate-Source Charge	$Q_{\text{gs}}$		---	10.6	---	
Gate-Drain Charge	$Q_{\text{gd}}$		---	34.7	---	
Turn-On Delay Time	$T_{\text{d}(\text{on})}$	$V_{\text{DS}}=-25\text{V}, V_{\text{GS}}=-10\text{V}, R_g=3\Omega, I_{\text{D}}=50\text{A}$	---	10	---	ns
Rise Time	$T_r$		---	50	---	
Turn-Off Delay Time	$T_{\text{d}(\text{off})}$		---	234	---	
Fall Time	$T_f$		---	155	---	
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=-15\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	---	5326	---	pF
Output Capacitance	$C_{\text{oss}}$		---	643	---	
Reverse Transfer Capacitance	$C_{\text{rss}}$		---	563	---	

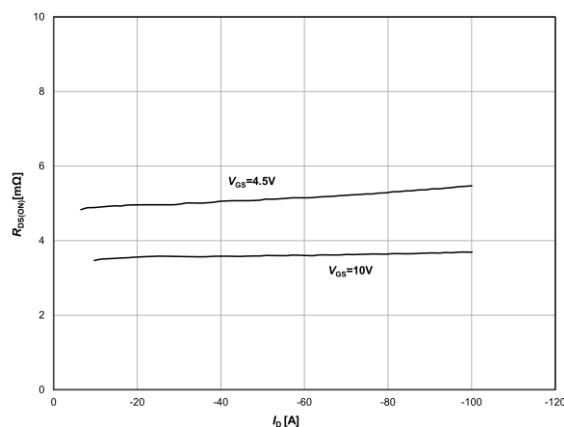
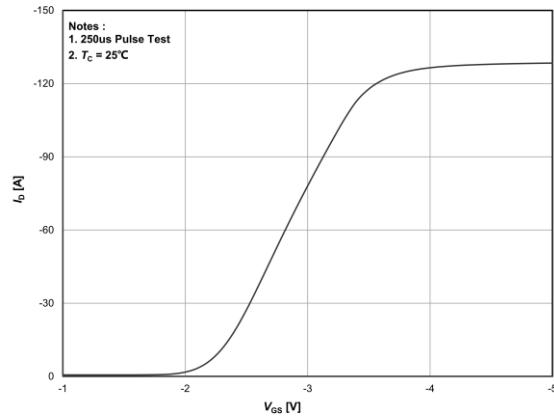
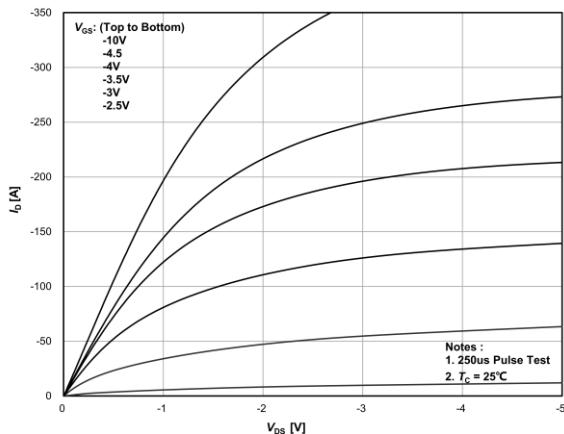
**Drain-Source Diode Characteristics**

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Continuous Source Current <sup>1</sup>	$I_s$		---	---	-122	A
Diode Forward Voltage <sup>2</sup>	$V_{\text{SD}}$	$V_{\text{GS}}=0\text{V}, I_s=-20\text{A}$	---	---	-1.1	V
Reverse Recovery Time	$t_{\text{rr}}$	$I_F=-50\text{A}, \text{di/dt}=460\text{A}/\mu\text{s}$	---	27	---	nS
Reverse Recovery Charge	$Q_{\text{rr}}$		---	0.07	---	nC
Peak Reverse Recovery Current	$I_{\text{rrm}}$		---	7	---	A

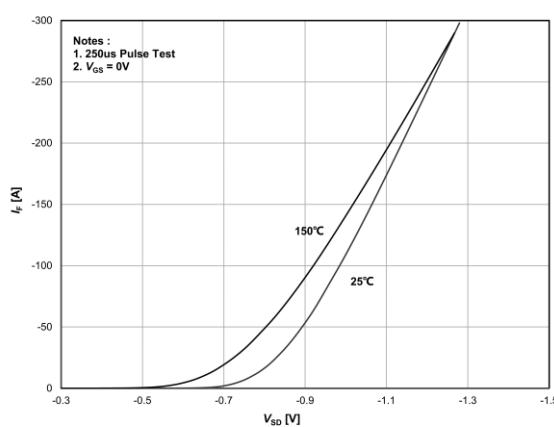
**Note:**

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is  $V_{\text{DD}}=-50\text{V}, V_{\text{GS}}=-10\text{V}, L = 0.5\text{mH}$
- 4.The power dissipation is limited by  $150^\circ\text{C}$  junction temperature

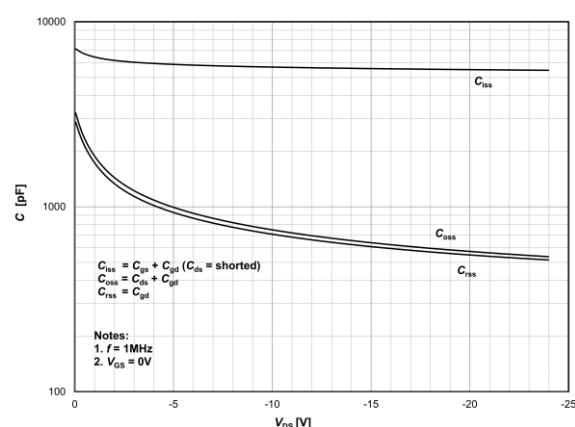
## Typical Characteristics



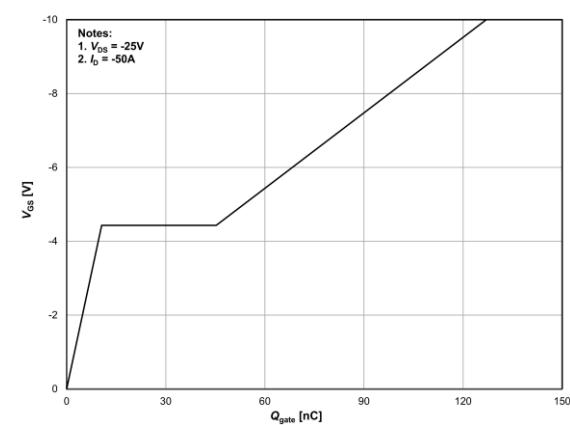
**Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage**



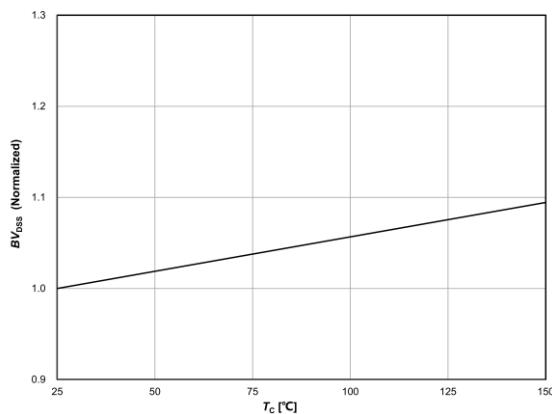
**Figure 4. Body Diode Forward Voltage Variation with Current**



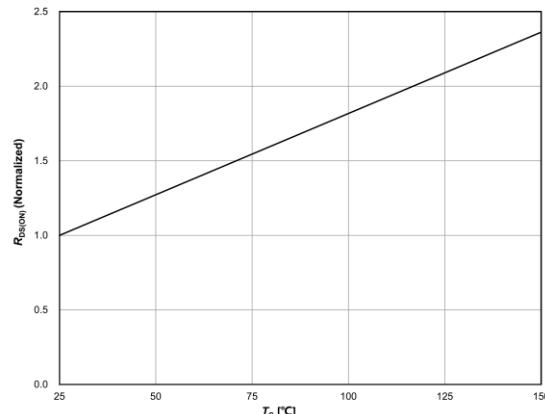
**Figure 5. Capacitance Characteristics**



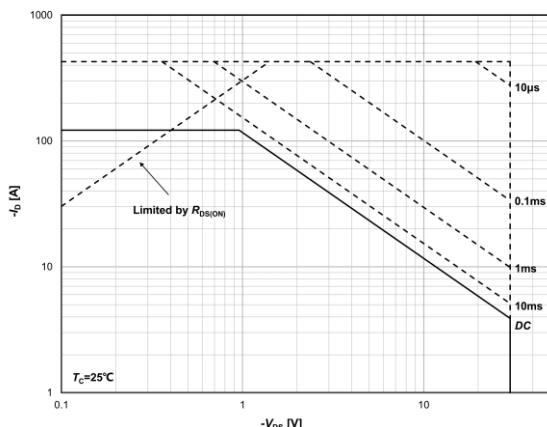
**Figure 6. Gate Charge Characteristics**



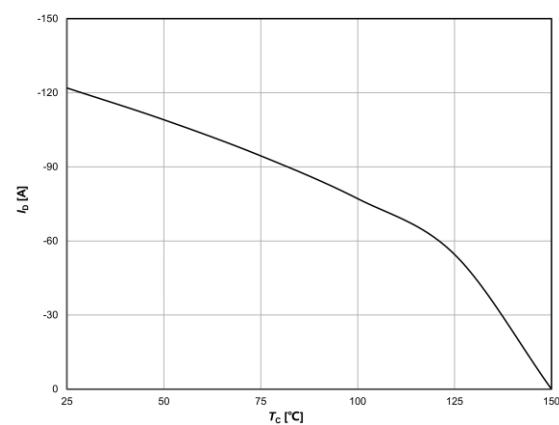
**Figure 7. Breakdown Voltage Variation  
vs Temperature**



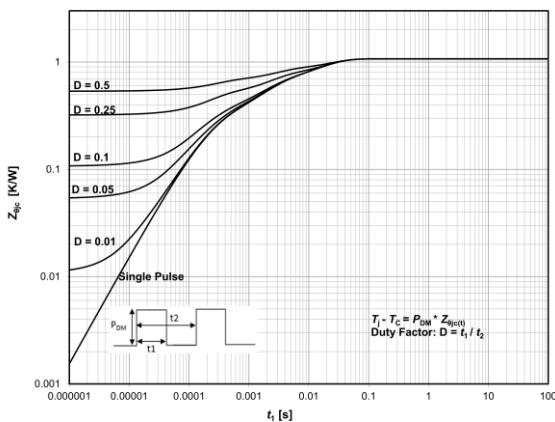
**Figure 8. On-Resistance Variation  
vs Temperature**



**Figure 9. Maximum Safe Operating Area<sup>3)</sup>**



**Figure 10. Maximum Drain Current  
vs Case Temperature**



**Figure 11. Transient Thermal Response Curve**

### TO-252 Package Outline Dimensions

Dim.	Min.	Max.
A	2.1	2.5
A1	6.3	6.9
B	0.96	1.42
B1	0.74	0.86
B2	0.74	0.94
C	Typ0.5	
D	5.33	5.53
D1	3.65	4.05
E	6.0	6.2
E1	Typ2.29	
E2	Typ4.58	
O	0	0.15
L1	9.9	10.5
L2	Typ1.65	
L3	0.6	1.0

All Dimensions in millimeter