

## Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

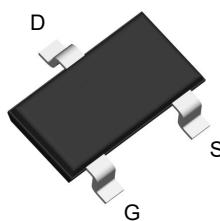
## Product Summary



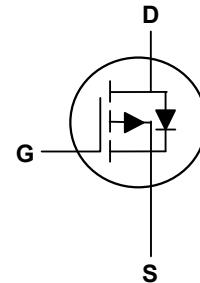
$V_{DS}$	-30	V
$I_D$	-4.1	A
$R_{DS(ON)}$ (at $V_{GS}=-10V$ )	65	mΩ
$R_{DS(ON)}$ (at $V_{GS}=-4.5V$ )	75	mΩ

## Applications

- High Frequency Point-of-Load,Synchronous Buck Converter
- Networking DC-DC Power System
- Load Switch



SOT23 Top View



## Absolute Maximum Ratings( $T_c=25^\circ C$ , unless otherwise noted)

Parameter	Symbol	Rating	Units
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	V
Continuous Drain Current <sup>1</sup>	$I_D @ T_c = 25^\circ C$	-4.1	A
Continuous Drain Current <sup>1</sup>	$I_D @ T_c = 100^\circ C$	-2.6	A
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	-16.4	A
Total Power Dissipation <sup>4</sup>	$P_D$	1.56	W
Storage Temperature Range	$T_{STG}$	-55 to 150	°C
Operating Junction Temperature Range	$T_J$	-55 to 150	°C

## Thermal Characteristics

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance Junction-Ambient <sup>1</sup>	$R_{\theta JA}$	---	80	°C/W

**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$\text{V}_{\text{GS}}=0\text{V}$ , $\text{I}_D=-250\mu\text{A}$	-30	---	---	V
Static Drain-Source On-Resistance	$\text{R}_{\text{DS}(\text{ON})}$	$\text{V}_{\text{GS}}=-10\text{V}$ , $\text{I}_D=-4\text{A}$	---	55	65	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=-4.5\text{V}$ , $\text{I}_D=-3\text{A}$	---	65	75	$\text{m}\Omega$
Gate Threshold Voltage	$\text{V}_{\text{GS}(\text{th})}$	$\text{V}_{\text{GS}}=\text{V}_{\text{DS}}$ , $\text{I}_D = -250\mu\text{A}$	-0.4	---	-0.9	V
Drain-Source Leakage Current	$\text{I}_{\text{DSS}}$	$\text{V}_{\text{DS}}=-30\text{V}$ , $\text{V}_{\text{GS}}=0\text{V}$	---	---	-1	$\mu\text{A}$
Gate-Source Leakage Current	$\text{I}_{\text{GSS}}$	$\text{V}_{\text{GS}}=\pm 12\text{V}$ , $\text{V}_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	$\text{nA}$
Total Gate Charge	$\text{Q}_g$	$\text{V}_{\text{DS}}=-15\text{V}$ , $\text{V}_{\text{GS}}=-4.5\text{V}$ , $\text{I}_D=-4\text{A}$	---	8	---	nC
Gate-Source Charge	$\text{Q}_{\text{gs}}$		---	1.9	---	
Gate-Drain Charge	$\text{Q}_{\text{gd}}$		---	1.4	---	
Turn-On Delay Time	$\text{T}_{\text{d}(\text{on})}$	$\text{V}_{\text{DS}}=-15\text{V}$ , $\text{V}_{\text{GS}}=-10\text{V}$ , $\text{R}_G=6\Omega$ , $\text{I}_D=-1\text{A}$	---	5.4	---	ns
Rise Time	$\text{T}_r$		---	19.4	---	
Turn-Off Delay Time	$\text{T}_{\text{d}(\text{off})}$		---	45.9	---	
Fall Time	$\text{T}_f$		---	12.4	---	
Input Capacitance	$\text{C}_{\text{iss}}$	$\text{V}_{\text{DS}}=-15\text{V}$ , $\text{V}_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	810	---	pF
Output Capacitance	$\text{C}_{\text{oss}}$		---	85	---	
Reverse Transfer Capacitance	$\text{C}_{\text{rss}}$		---	50	---	

**Drain-Source Diode Characteristics**

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Continuous Source Current <sup>1</sup>	$\text{I}_s$	$\text{VG}=\text{VD}=0\text{V}$ , Force Current	---	---	-4.1	A
Pulsed Source Current <sup>2,5</sup>	$\text{I}_{\text{sm}}$		---	---	-16.4	A
Diode Forward Voltage <sup>2</sup>	$\text{V}_{\text{SD}}$	$\text{V}_{\text{GS}}=0\text{V}$ , $\text{I}_s=-1\text{A}$	---	---	-1	V

**Note:**

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$
- 3.The power dissipation is limited by  $150^\circ\text{C}$  junction temperature

## Typical Characteristics

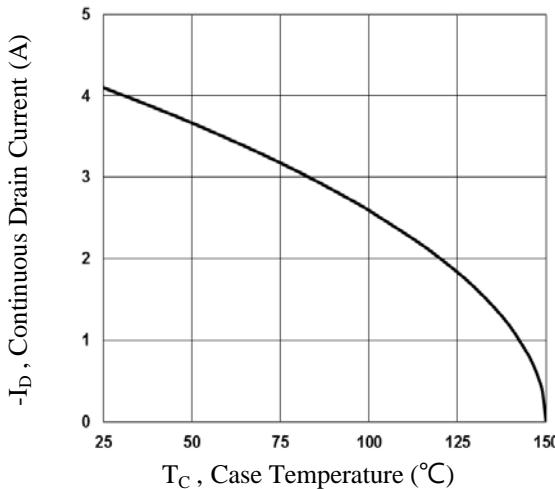


Fig.1 Continuous Drain Current vs. T<sub>c</sub>

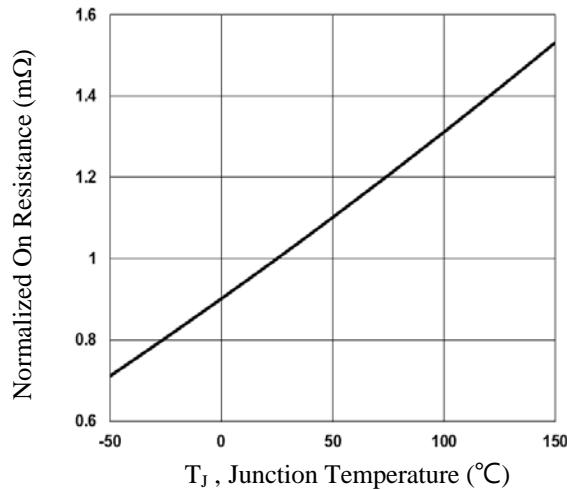


Fig.2 Normalized RDS(on) vs. T<sub>j</sub>

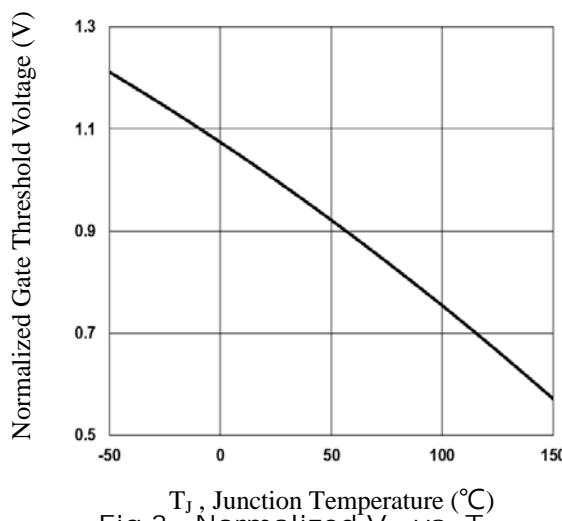


Fig.3 Normalized V<sub>th</sub> vs. T<sub>j</sub>

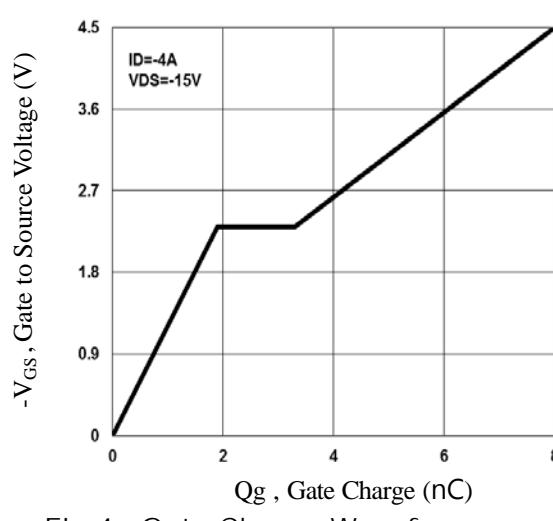


Fig.4 Gate Charge Waveform

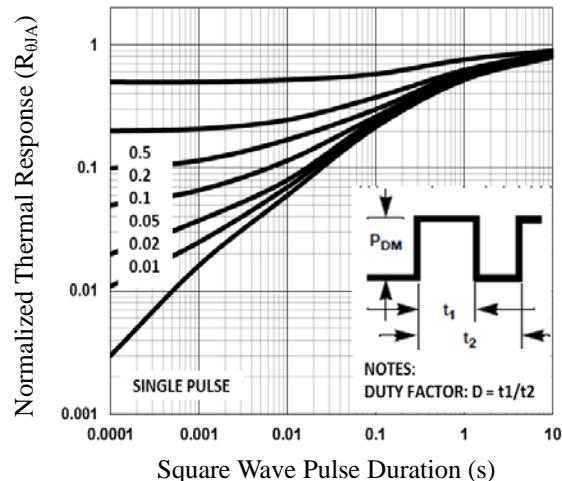


Fig.5 Normalized Transient Impedance

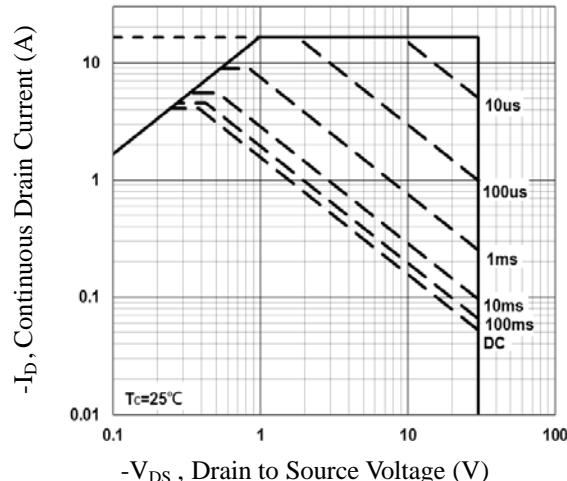


Fig.6 Maximum Safe Operation Area

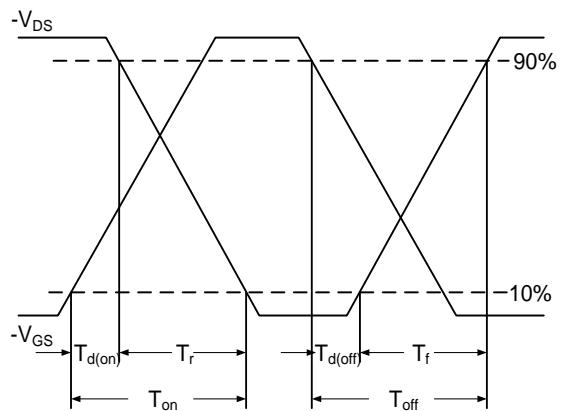


Fig.7 Switching Time Waveform

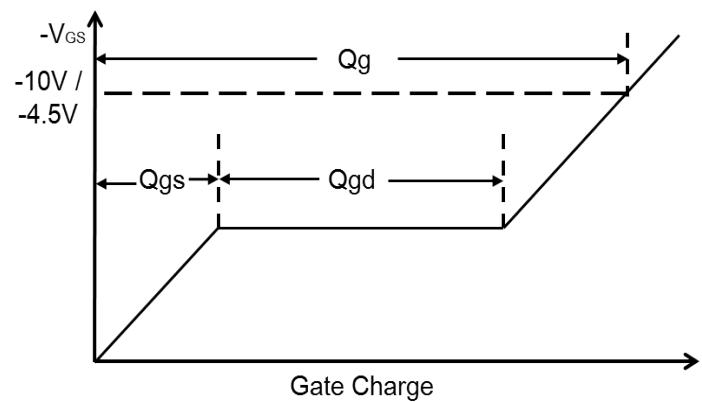
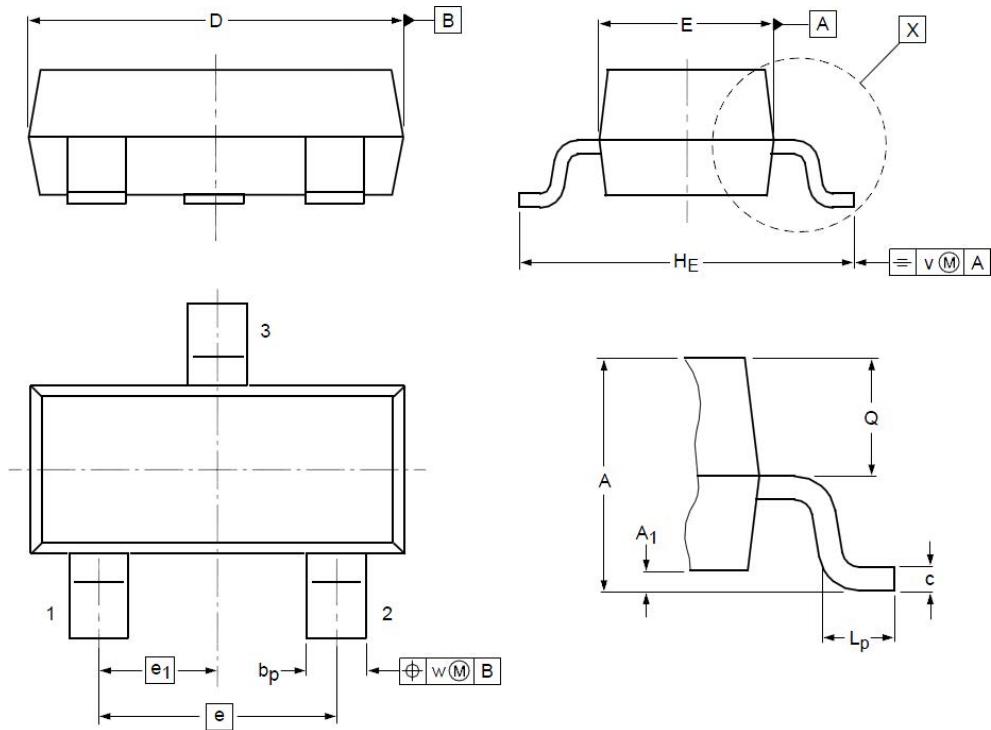


Fig.8 Gate Charge Waveform

### SOT23 Package Outline Dimensions



<b>Symbol</b>	<b>Dimensions (unit:mm)</b>			<b>Symbol</b>	<b>Dimensions (unit:mm)</b>		
	<b>Min</b>	<b>Typ</b>	<b>Max</b>		<b>Min</b>	<b>Typ</b>	<b>Max</b>
<b>A</b>	0.90	1.05	1.20	<b>e<sub>1</sub></b>	--	0.95	--
<b>A<sub>1</sub></b>	0.01	0.05	0.10	<b>H<sub>E</sub></b>	2.10	2.40	2.50
<b>b<sub>p</sub></b>	0.38	0.42	0.48	<b>L<sub>p</sub></b>	0.40	0.50	0.60
<b>c</b>	0.09	0.13	0.15	<b>Q</b>	0.45	0.49	0.55
<b>D</b>	2.80	2.92	3.00	<b>V</b>	--	0.20	--
<b>E</b>	1.20	1.33	1.40	<b>W</b>	--	0.10	--
<b>e</b>	--	1.90	--				